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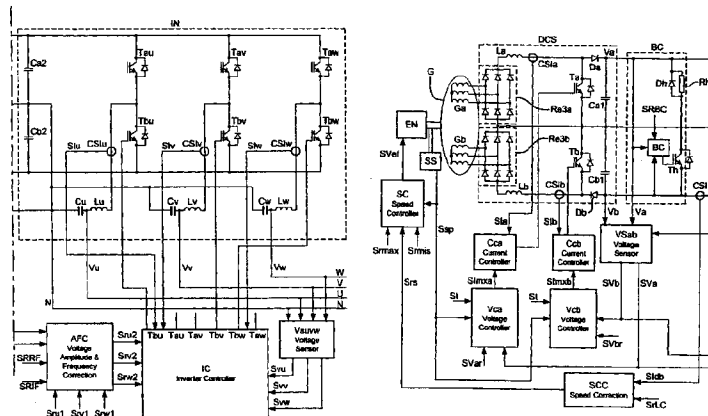
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(54) Title: AN AC POWER GENERATING SYSTEM



(57) Abstract: An electric power generating set which automatically varies its speed to match load variations at any time includes an engine driven generator G which provides a positive, Ga and a negative, Gb AC output voltage each of which is rectified by a respective rectifier Re3a, Re3b. Each rectifier Re3a, Re3b has one terminal connected to a common neutral line N. The DC output voltage of each rectifier Re3a, Re3b is boosted in a respective booster circuit which also has one terminal connected to the neutral line N. The combined output of the two booster circuits, Va + Vb is fed to an Inverter IN which converts it to the AC power output of the generating set. A DC load current between the booster circuits and the inverter IN is monitored and used to provide a speed demand feedback signal Srs to the speed control of the engine EN. The inverter IN is controlled so that the voltage and frequency of the AC power output of the generating set are reduced in response to a fall in the intermediate DC voltage Va + Vb that is fed to the inverter IN and which is monitored by a voltage sensor Vsa.



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## AN AC POWER GENERATING SYSTEM

This invention relates to an AC power generating system in which an AC power output is produced by conversion from an intermediate DC output.

5 WO98/28832 discloses a generating set which comprises an engine/generator which provides a variable voltage electrical output, this output being rectified and fed to a DC to DC converter, the output DC voltage of the DC to DC converter being monitored by a control circuit and serving as an intermediate DC output which is fed to an inverter to generate an AC output which supplies an external load. The DC to DC converter decouples or isolates  
10 the intermediate DC output from the fluctuations in the current and/or voltage output of the generator so that the apparatus is able to accommodate substantial variations in the output of the generator while maintaining the intermediate DC output within desired operating parameters. The DC to DC converter also serves to decouple or isolate the generator from variations in the load. An energy storage device, for example a capacitor, is connected in  
15 parallel with the DC link and thereby provides a short term energy reserve when the load applied to the DC link voltage varies suddenly. This energy storage device may be supplemented by other similar devices with respective control systems. In some cases such supplementary energy storage devices may be excluded. Instead a load anticipation circuit may be introduced to cope with high impact or step loads.

20 The concept of power generation by such a generating set which automatically varies its speed to match load variations at any time is to operate the prime mover at an optimum speed, based on efficiency or fuel economy or low noise or any other desirable parameter, while providing the required engine kW output to satisfy the load demand and any losses in the system. To achieve this, the control circuit includes various electronic controllers which  
25 monitor and control the intermediate DC output or link voltage across a capacitor, through a voltage sensor and a feedback signal voltage.

The measurement and control circuit of the generating set disclosed in WO98/28832 monitors the generator output voltage and current together with the DC link voltage and operates through a feedback control loop to vary the speed of the engine so as maintain the  
30 DC link voltage constant and limit the generator output current to a level determined a current reference signal.

US-A-5,563,802 discloses an integrated power system which includes an engine/generator power supply integrated with storage batteries to provide household-like AC power. For a small load demand, the storage batteries are used to provide electricity.  
35 For a large load demand, the engine/generator starts to supply electricity. The engine/generator also recharges the storage batteries if the load demand is smaller than the

engine/generator maximum load capacity. For a larger load demand, both the engine/generator and the storage batteries supply electricity in parallel. The engine speed is variable for various loads. A pulse-width modulated signal is generated which monitors the DC voltage of the high voltage DC bus. This arrangement provides the variable engine speed system by speeding up or slowing down as the system power requirements change so as to maintain the voltage on the high voltage DC bus substantially constant. The bigger the load, the higher the engine speed that is required to produce the necessary power and keep a constant DC voltage.

US-A-5,198,698 discloses an auxiliary power supply system including an internal combustion engine driving an alternator the output of which is rectified and supplied to DC bus lines across which a storage battery is connected. The DC bus lines may supply a consuming device such as an uninterruptible power supply or telecommunications system. The voltage across the bus lines is sensed and when the voltage drops below a selected value, indicating that the consuming device is drawing power from the battery beyond a desired limit, the engine is turned on for a period of time to warm it up, during which the power from the generator is not supplied to the DC bus lines. Thereafter, the generator supplies power to the DC bus lines to supply the consuming device and partially recharge the battery until the consuming device is no longer drawing power, after which the engine is shut off. The power supply system includes a controller which monitors the intervals of time between turn-ons of the engine and if a selected period of time is exceeded, the engine is started without supplying power from the generator to the DC bus lines to allow the engine and generator to warm-up, enhancing maintenance of the system. The operating characteristics of the engine and generator are sensed and stored for access by an operator and warnings are provided if these conditions exceed acceptable limits.

One object of one aspect of this invention is to enable operation of such a generating set without there having to be an energy storage device connected in parallel with the DC link to provide a short term energy reserve when the load applied to the DC link voltage varies suddenly.

According to one aspect of this invention there is provided an AC power generating system including inverter means which are operable to convert an intermediate DC voltage into an AC power output, means for generating and maintaining the intermediate DC voltage at one level and voltage sensor means operable to monitor the intermediate DC voltage, wherein control means are provided for the inverter means, said control means being responsive to the voltage sensor means and being operable to control operation of the inverter means so the voltage and/or the frequency of the AC power output is/are reduced in response to a fall in the intermediate DC voltage to a certain level caused by the application

of a step load to the AC power output, whereby to provide a transient off-loading effect which will give the generating means time to respond and thereby allow the intermediate DC voltage to be restored to said one level.

The means for generating and maintaining the intermediate DC voltage of a preferred embodiment of this aspect of the invention include generator means operable to  
5 generate a variable voltage AC power supply and rectifier means having an output and operable to rectify the variable voltage AC power supply to establish the intermediate DC voltage.

Conveniently the generator means are driven by a variable speed prime mover, the  
10 control means of the generator means comprising speed control means operable to control the speed of the prime mover. Preferably the generator means are a prime mover driven permanent magnet generator.

If the neutral of the AC output of the generating set disclosed by WO98/28832 were to be connected to the neutral terminal of the generator, the variable voltage three-phase  
15 output of the generator would have to be rectified by a half wave rectifier. As a result large and costly inductors would need to be used for the intermediate DC link. Furthermore, there would be a risk that a DC component could be established in the windings of the generator. This could result in torque pulsations due to the DC component of the generator current.

US-A-4,507,724 discloses a polyphase inverter system which converts DC power  
20 developed by a DC source into polyphase AC power to drive a load. The DC source may be a generator and rectifier arrangement which includes control and protection circuitry for operating the generator. In any event, the DC source includes two terminals at first and second voltages and a neutral or common terminal which is at a voltage midway between the first and second voltages. The neutral of the polyphase AC power developed by the  
25 inverter is connected to the neutral or common terminal of the DC source.

US-A-3,775,663 discloses a solid state inverter having an electronic neutral which is an additional single phase inverter operated as a current source or sink to maintain the potential at the neutral terminal at a controlled voltage independent of neutral current flow into and out of the neutral terminal. The inverter has input terminals which are adapted  
30 to be connected to a battery, a rectifier energized by an alternating voltage or some other source of unidirectional voltage.

A neutral setting of the AC power output could be generated and regulated by a controlled division of the intermediate DC voltage in which case the rectifier means could be full wave rectifier means. Accordingly a full-wave ripple voltage could be maintained in the  
35 DC link so that establishment of a DC component in the windings of the permanent magnet generator could be avoided.

Booster means having an input which is connected to the output of the rectifier means and which are operable to increase the voltage of the rectified output of said rectifier means and thereby to establish the intermediate DC voltage may be provided. The generator means may include respective control means and the voltage sensor means may provide a  
5 feedback control signal to the control means of the generator means whereby to effect variation of the variable AC power supply and thereby to counter a tendency of the intermediate DC voltage to vary.

In a preferred embodiment, current sensor means operable to monitor a DC load current caused by connection of a load across the intermediate DC voltage are provided, the  
10 current sensor means being operable to emit a signal which is indicative of the monitored DC load current, comparator means being provided for comparing an output signal from the current sensor means with a reference signal and for emitting a speed correction signal which is proportional to the amount by which the signal from the current sensor means exceeds the reference, that output from the comparator means being supplied to the speed  
15 control means of the generator means to effect an increase in the speed of the prime mover above that required for the applied load.

The AC power generating system may include brake control means and means responsive to the output of the voltage sensor means that is operable to monitor the intermediate DC voltage whereby to connect the brake control means across the intermediate  
20 DC voltage to apply a load thereto when the intermediate DC voltage rises to a certain level. The control means for the inverter means may be operable to increase the frequency of the AC power output in response to an increase in the intermediate DC voltage to a certain high level. The certain high level conveniently is higher than the level at which the brake control means is connected across the intermediate DC voltage.

25 In order to provide a short term supply of electrical energy when the DC link voltage tends to fall in response to a high load demand electrical energy storage means may be connected across the intermediate DC voltage so as to be charged by the intermediate DC voltage, and bistable switching means and associated control means may be provided, said bistable switching means normally being in one state in which they are operable to make the  
30 connection of the electrical energy storage means across the intermediate DC voltage to enable the electrical energy storage means to be charged and to interrupt a connection between the electrical energy storage means and the connection between the output of the rectifier means and the input of the booster means, said bistable switching means being operable in its other state to isolate the electrical energy storage means from the intermediate  
35 DC voltage and to make said connection of the electrical energy storage means with the connection between the output of the rectifier means and the input of the booster means

whereby to enable discharge of electrical energy from said electrical energy storage means into the connection between the rectifier means and the booster means, said control means associated with the bistable switching means being responsive to the monitored intermediate DC voltage and being operable in response to a fall in said intermediate DC voltage to a  
5 certain reference level to switch said bistable switching means from its normal said one state to said other state whereby to discharge electrical energy to augment the rectified output of said rectifier means and thereby to counter the sensed fall in said intermediate DC voltage.

Hence the current to be supplied by the electrical energy storage means to augment the rectified output of the rectifier means need not be as large as it would need to be were it  
10 to be supplied to the DC link between the booster means and the inverter means as in the system disclosed in WO98/28832.

Varying the speed of the engine with changes in the load by using a feedback signal derived from monitoring the intermediate DC link voltage as taught by WO98/28832 or by US-A-5,563,802 or by monitoring either the voltage or current of the variable voltage  
15 electrical output of the engine driven generator as taught by WO98/28832 is liable to result in unstable operation such as hunting because the sensed parameter does not respond directly to the external load.

WO98/07224 discloses a power conversion system which responds to an abnormal output condition such as overcurrent by one or more of a number of techniques  
20 including reducing engine speed. The system includes a control circuit, an engine, a generator, a controlled rectifier that provides a rectified signal, a power converter and a detector. It is a fault protection system

GB-A-2,318,000 discloses a UPS system with a battery and an inverter. It includes a microprocessor controlled monitoring facility which measures the voltages and  
25 currents including current fed to a power converter so that necessary adjustments and/or warnings can be made. The output of the inverter is regulated and stabilized.

An object of another aspect of this invention is to provide means for varying the speed of the engine with changes in the external load which operate in a stable manner.

According to that other aspect of the invention, there is provided an AC power  
30 generating system including generator means operable to generate a variable voltage AC power supply, rectifier means having an output and being operable to rectify the variable voltage AC power supply, booster means having an input which is connected to the output of the rectifier means and which are operable to increase the voltage of the rectified output of said rectifier means to establish an intermediate DC voltage, a variable speed prime  
35 mover drivingly coupled with the generator means, and speed control means operable to control the speed of the prime mover, wherein current sensor means are provided, said

current sensor means being operable to monitor a DC load current caused by connection of a load across the intermediate DC voltage, the current sensor means being operable to emit a signal which is indicative of the monitored DC load current, comparator means being provided for comparing an output signal from the current sensor means with a reference  
5 signal and for emitting a speed correction signal which is proportional to the amount by which the output signal from the current sensor means exceeds the reference signal, the output from the comparator means being supplied to the speed control means for the generator means to effect an increase in the speed of the variable speed prime mover.

An object of a further aspect of this invention is to provide an improved  
10 arrangement for providing a short term supply of electrical energy when the DC link voltage tends to fall in response to a high load demand.

According to the further aspect of this invention there is provided, an AC power generating system including generator means operable to generate a variable voltage AC power supply, rectifier means having an output and operable to rectify the variable voltage  
15 AC power supply, booster means having an input which is connected to the output of the rectifier means and which is operable to increase the voltage of the rectified output of said rectifier means and thereby to provide an intermediate DC voltage, inverter means operable to convert the intermediate DC voltage into an AC power output for supplying to an external load, voltage sensor and control means operable to monitor the intermediate DC voltage and  
20 to provide a feedback control signal to the generator means whereby to vary the variable voltage AC power supply so as to counter a tendency of the intermediate DC voltage to vary, and electrical energy storage means connected across the intermediate DC voltage so as to be charged by the intermediate DC voltage, wherein bistable switching means and associated control means are provided, said bistable switching means normally being in one  
25 state in which they are operable to make the connection of the electrical energy storage means across the intermediate DC voltage to enable the electrical energy storage means to be charged and to interrupt a connection between the electrical energy storage means and a connection between the generator means and the input of the booster means, said bistable switching means being operable in its other state to isolate the electrical energy storage  
30 means from the intermediate DC voltage and to make said connection of the electrical energy storage means with the connection between the generator means and the input of the booster means, whereby to enable discharge of electrical energy from said electrical energy storage means into the connection between the generator means and the booster means, said control means associated with the bistable switching means being responsive to the  
35 monitored intermediate DC voltage and being operable in response to a fall in said intermediate DC voltage to a certain reference level to switch said bistable means from its



normal said one state to said other state whereby to discharge electrical energy to augment the variable voltage AC power supply and thereby to counter the sensed fall in said intermediate DC voltage.

Preferably the connection between the generator means and the input of the booster means is the connection between the rectifier means and the input of the booster means so that the discharged electrical energy augments the rectified output of said rectifier.

A neutral setting of the AC power output could be generated and regulated by a controlled division of the intermediate DC link voltage before it is converted into the AC power output in which case the connection between the electrical energy storage means and the connection between the output of the rectifier means and the input of the booster means need only be made with one of the positive and negative connections between the rectifier means and the booster means so that the number of components required is reduced and cost is saved.

An object of another aspect of this invention is to enable smaller, less expensive inductors to be used in the DC link.

Although generation and regulation of a neutral setting of the AC power output by a controlled division of the voltage of the DC power before that power is converted to AC would enable the use of smaller less expensive inductors, it has the drawback that the capacitors that are connected in series across the DC voltage to effect that division are also connected across the load and therefore must be highly rated so that each of them is capable of withstanding the full load to avoid the danger of capacitor damage in the event of one of them being short circuited. Furthermore, although the power generating system is designed so that the intermediate DC voltage is decoupled or isolated from fluctuations in the current and/or voltage output of the generator so that the apparatus is able to accommodate substantial variations in the output of the generator whilst maintaining the intermediate DC output within desired operating parameters, there are liable to be transient variations in the latter with variations in the load. Hence there may be transient variations in a neutral setting produced by a controlled division of the intermediate DC voltage. As stated above, US-A-4,507,724 discloses a DC source which develops DC power which is converted into polyphase AC power, the DC source including two terminals at first and second voltages and a neutral or common terminal which is at a voltage midway between the first and second voltages.

Accordingly, an object of another aspect of this invention is to enable the use of full wave rectification whilst providing a reliably constant neutral setting which will serve as a good reference potential and which will reduce the need for expensive, highly-rated capacitors.

According to yet another aspect of this invention there is provided an AC power generating system including variable voltage AC power supply generator means, rectifier means having an output and operable to rectify the variable voltage AC power supply to establish an intermediate DC voltage, inverter means which are operable to convert the  
5 intermediate DC voltage into an AC power output, voltage sensor means operable to monitor the intermediate DC voltage and control means responsive to an output from the voltage sensor means and operable to maintain the intermediate DC voltage at one level, wherein the generator means is operable to generate two variable voltage AC power supplies, the rectifier means comprise two full-wave rectifiers, each full-wave rectifier being operable to rectify a  
10 respective one of the two variable voltage AC power supplies and each being connected on one side to a neutral terminal and having an output voltage terminal on its other side, the output voltage terminal of one of the full-wave rectifiers being positive and the output voltage terminal of the other full-wave rectifier being negative such that the two variable voltage AC power supplies are severally rectified to produce a positive and a negative  
15 potential respectively which together comprise the intermediate DC voltage, the voltage sensor means comprising two voltage sensors severally responsive to a respective one of the positive and negative output potentials of the two full-wave rectifiers and the control means comprising two controllers severally operable to maintain each of those positive and negative potentials at a certain level whereby to maintain the intermediate DC voltage at said one  
20 level.

Preferably there are two booster circuits, each connected between a respective one of the output voltage terminals of the two full-wave rectifiers and the neutral terminal and each operable to increase the voltage of the respective output potential and thus to increase the intermediate DC voltage, each of the controllers being operatively associated with the  
25 respective one of the booster circuits.

The generator means which may be a permanent magnet generator, may be driven by a variable speed prime mover which may be provided with speed control means operable to control the speed of the prime mover. Preferably current sensor means operable to monitor a DC load current caused by connection of a load across the intermediate DC  
30 voltage are provided, the current sensor means being operable to emit a signal which is indicative of the monitored DC load current, comparator means being provided for comparing an output signal from the current sensor means with a reference signal and for emitting a speed correction signal which is proportional to the amount by which the signal from the current sensor means exceeds the reference, that speed correction signal being  
35 supplied to the speed control means for the generator as a feed back control signal whereby to effect variation of the variable AC power supply and thereby to counter any tendency of

the intermediate voltage to vary.

The AC power generating system may include brake control means and means responsive to the output of the two voltage sensors that are operable to monitor the positive and negative output potentials whereby to connect the brake control means across the intermediate DC voltage to apply a load thereto when the potential difference between the monitored positive and negative potentials rises to a certain level. Preferably one pair of capacitors are connected in parallel between the positive output potential and the neutral terminal, another pair of capacitors is connected in parallel between the negative output potential and the neutral terminal and the brake control means are connected across the positive and negative output potentials between the capacitors of each pair. Preferably the current sensor means that are operable to monitor the DC load current caused by connection of a load across the intermediate DC voltage are connected in either the positive or the negative connection between the brake control means and the capacitors of each pair that are remote from the two full-wave rectifiers and that are nearer to the inverter means.

The power supply system disclosed in US-A-5,563,802 discussed above has microprocessor control means for integrating the battery and the engine/generator according to a predetermined control logic. The microprocessor monitors various parameters. It shuts down the engine/generator if certain parameters are exceeded. For example:

- 1) the speed of the engine/generator exceeds a predetermined limit (overspeed controlling);
- 2) the output of the inverter circuitry exceeds a predetermined limit (called chopper overvoltage);
- 3) the voltage output of the engine/generator exceeds a predetermined limit (called engine generator overvoltage); and
- 4) the boost voltage at the input of the inverter circuitry exceeds a predetermined limit (called booster overvoltage).

The microprocessor also activates the battery voltage booster upon a current sensor detecting a transient condition caused by addition of electrical load so as to overcome undershoots and activates charging of the battery upon the current sensor detecting a transient condition caused by deletion of an electrical load so as to overcome overshoots.

US-A-5,606,244 discloses an AC power generating system which includes an engine driven generator, a rectifier, a booster which boosts the rectified output of the engine/generator if it is less than a predetermined value so as to generate a stable DC voltage at a suitable level which it feeds it to an inverter to produce an AC output. The voltage fed to the inverter is monitored and used as a feedback signal for the booster. The system includes electronic protection against over current, over load, short circuit and

thermal over load conditions.

As stated above, US-A-5,198,698 discloses an auxiliary power supply which supplies DC voltage to a consuming device such as a UPS system. GB-A-2,318,000 and US-A-5,811,960 disclose UPS systems, the latter being a battery-less system.

5 According to yet another aspect of this invention, there is provided a method of operating an AC power generating system including the steps of :-

- (i) operating a variable speed prime mover driven generator to generate a variable voltage AC power supply;
- (ii) rectifying the variable voltage AC power supply to establish an intermediate DC  
10 voltage;
- (iii) controlling the intermediate DC voltage to maintain it at a substantially constant level;
- (iv) operating an inverter to convert the intermediate DC voltage into an AC power output; and
- 15 (v) monitoring the intermediate DC voltage and DC current flow to the inverter; wherein the monitored DC voltage and DC current are compared with a number of references whereby, whenever the one of the monitored DC voltage and DC current deviates from an acceptable normal level at least transiently, an appropriate control correction is applied automatically, the nature of the applied control correction depending on the severity  
20 of the deviation from the acceptable level.

According to a further aspect of this invention, there is provided a method of operating an AC power generating system including the steps of:-

- (i) operating a variable speed prime mover driven generator to generate a variable voltage AC power supply;
- 25 (ii) rectifying the variable voltage AC power supply to establish an intermediate DC voltage;
- (iii) controlling the intermediate DC voltage to maintain it at a substantially constant level;
- (iv) operating an inverter to convert the intermediate DC voltage into an AC power  
30 output;
- (v) monitoring the intermediate DC voltage;
- (vi) comparing the intermediate DC voltage with a first reference voltage which is lower than the substantially constant level; and
- (vii) controlling operation of the prime mover driven generator by feedback control so  
35 as to increase its speed when the monitored intermediate DC voltage falls to the level of the first reference voltage whereby to restore the intermediate DC voltage to said substantially

constant level; wherein

(viii) the intermediate DC voltage is also compared with a second reference voltage which is lower than said first reference voltage, and

- (ix) operation of the inverter is controlled so as to reduce the voltage and/or  
5 frequency of the AC power output when the monitored intermediate DC voltage falls to the level of the second reference voltage whereby to provide a transient off-loading effect which assists restoration of the intermediate DC voltage to said substantially constant level.

Preferably the method of operating an AC power generating system includes the further steps of:-

- 10 a) monitoring a DC load current which results from connection of a load across the intermediate DC voltage;  
b) comparing the monitored DC load current with a certain current reference level; and  
c) further controlling operation of the prime mover driven generator by feedback control when the monitored DC load current exceeds the certain current reference level whereby to  
15 further increase the speed of the prime mover driven generator by an amount which is proportional to the amount by which the monitored DC load current exceeds the certain current level.

Operation of the inverter may further be controlled so as to increase the frequency of the AC power supply when the monitored DC voltage rises to the level of a third  
20 reference voltage. A brake controller may be connected across the monitored DC voltage to apply a load thereto when the monitored DC voltage rises to a certain voltage level. Conveniently the third reference voltage is higher than said certain voltage level.

In order to convert a DC voltage into a multiphase AC power output, it is usual to provide a pair of transistors for each phase, that pair of transistors being arranged so that the  
25 collector of one of them is connected to a positive of the DC voltage, the emitter of the other is connected to a negative of the DC voltage and the emitter of said one transistor is connected to the collector of said other transistor. The connection between the two transistors for each phase is connected through an inductance, or choke of an LC output filter to an AC power output terminal for the respective phase. Operation of the transistors  
30 is controlled by a controller which generates a pulse width modulated drive to the base of the respective transistor to control its operation. The basis of this control involves operating the transistors so as to connect the positive and negative potentials of the DC voltage alternately to the respective AC power output terminal through the inductance or choke of the LC output filter that smoothes it. This converter arrangement is adapted so as to  
35 minimise the number of power electronic devices and microprocessor controllers that are used. However the high frequency switching of the voltage between positive and negative

potentials results in the production of a high ripple current. This ripple current is passed through the transistors, inductances or chokes, the LC output filter capacitors and the capacitors that are connected across the DC voltage. As a result it is necessary to employ high capacity DC capacitors in order to cater for the high ripple current. Furthermore ripple currents which oscillate between positive and negative current values result in bi-directional power flow between the capacitors that are connected across the DC voltage and the capacitors of the LC output filters.

An object of another aspect of this invention is to provide an arrangement for converting a DC voltage to an AC power output in which lower ripple currents are generated.

According to yet another aspect of this invention there is provided a method of converting a DC voltage having a positive and negative level of electrical potential into an AC power output in which the positive and negative levels of electrical potential are connected to an AC power output terminal alternately, wherein each connection of one of the positive and negative levels of electrical potential to the output terminal is separated in time from a connection of the other of the positive and negative levels of the electrical potential to the output terminal by an intermediate period of zero voltage.

Where the source of each of the positive and negative potentials of DC voltage is a respective charged capacitor and the positive and negative levels of electrical potential are connected alternately to the AC power output terminal through an output filter, both sides of the output filter may be connected to neutral for said intervening periods.

According to a further aspect of this invention there is provided a DC to AC converter including a first source of electrical energy operable to be charged with a positive DC potential, a second source of electrical energy operable to be charged with a negative DC potential, first bistable switching means operable in one state to connect the first electrical energy source to an AC power output terminal through an inductor means of an output filter means and to break that connection in its other state, second bistable switching means operable in one state to connect the second electrical energy source to the AC power output terminal through said inductor means and to break that connection in its other state, third bistable switching means operable in one state to connect said inductor means to neutral and to break that connection in its other state, and control means operable to control operation of said first, said second and said third bistable switching means so that said first and said second bistable switching means are switched to their said one state alternately and are in their said other state when the other of said first and second bistable switching means are in their said one state and said third bistable switching means is switched to its said one state whilst said first and second bistable switching means are both switched to their said

other state between each alternate switching of said first and second bistable switching means to their said one state so that the positive and negative levels of electrical potential with which said first and second electrical energy sources are charged when said converter is operated are connected to the AC power output terminal alternately and each connection of  
5 the one of the positive and negative levels of electrical potential to the output terminal is separated in time from a connection of the other of the positive and negative levels of electrical potential to the output terminal by an intervening period of zero voltage.

Preferably the output filter means includes a third chargeable source of electrical energy which is connected on one side to the inductor means and on its other side to neutral,  
10 the third bistable switching means being operable to connect the other side of the inductor means to neutral when in its said one state.

In a preferred embodiment of this invention the DC to AC converter is the inverter means of an AC power generating system which includes variable voltage AC power supply generating means, rectifier means having an output and operable to rectify the  
15 variable voltage AC power supply to establish an intermediate DC voltage, the inverter means being operable to convert the intermediate DC voltage into the AC power output, there being voltage sensor means operable to monitor the intermediate DC voltage and control means responsive to an output of the voltage sensor means and operable to maintain the intermediate DC voltage at one level. Preferably the generator means is operable to  
20 generate two variable voltage AC power supplies and two full wave rectifiers are provided, each being operable to rectify a respective one of the two variable voltage AC power supplies and each being connected on one side to a neutral terminal and having an output terminal on its other side, the output terminal of one of the fullwave rectifiers being positive and the output terminal of the other fullwave rectifier being negative such that the two  
25 variable voltage AC power supplies are rectified to produce a positive and a negative potential respectively which together comprise the intermediate DC voltage and which are respectively connected across the first and second electrical energy storage means, there being voltage sensor means severally responsive to a respective one of the positive and negative output potentials of the two fullwave rectifiers and control means operable to  
30 maintain each of those positive and negative potentials at a certain level whereby to maintain the intermediate DC voltage at one level.

Preferably there are two booster circuits, each connected between a respective one of the output terminals of the two fullwave rectifiers and the neutral terminal and each operable to boost the respective one of the output electrical potentials, each of the control means  
35 being operatively associated with a respective one of the booster circuits.

According to yet another aspect of this invention there is provided a method of

operating an AC power generating system of the kind which includes an engine driven generator which provides a variable voltage electrical output, rectifier means operable to rectify the variable voltage electrical output, booster means operable to boost the voltage of the variable voltage electrical output and thereby to provide an intermediate DC voltage, inverter means which are operable to convert the intermediate DC voltage into an AC power output, means operable to maintain the intermediate DC voltage at one level up to a predetermined speed of the engine driven generator which is less than the maximum speed thereof, wherein the means operable to maintain the intermediate DC voltage at said one level is disabled when the speed of the engine driven generator rises to at least said predetermined speed and the speed of the engine driven generator and the intermediate DC voltage are allowed to rise so that , when the engine driven generator is running at maximum speed, the AC power output is allowed to increase until a balance is achieved.

An electric power generating set which automatically varies its speed to match load variations at any time in which this invention is embodied and several modifications of that generating set will be described now by way of example with reference to accompanying drawings of which:

Figures 1A and 1B together comprise a circuit diagram of the generating set, the junction between the two parts of the circuit diagram being shown by a chain dotted line in each of Figures 1A and 1B;

Figure 2 illustrates the control strategy of the generating set illustrated in Figures 1A and 1B;

Figure 3 is a circuit diagram of one modification of the generating set illustrated in Figures 1A and 1B, certain details of the circuit diagram shown in Figures 1A and 1B being omitted for the sake of clarity;

Figure 4 illustrates the control strategy of the generating set modified as illustrated in Figure 3;

Figure 5 is a circuit diagram similar to Figure 3, of another modification of the generating set illustrated in Figures 1A and 1B;

Figure 6 is an illustration of the control strategy of the generating set illustrated in Figure 5;

Figures 7A and 7B together comprise a circuit diagram of a further modified form of the generating set illustrated in Figures 1A and 1B, certain details of the circuit diagram shown in Figures 1A and 1B being omitted for the sake of convenience, the junction between the two parts of the circuit diagram being shown by a chain dotted line in Figures 7A and 7B;

Figure 8 is an illustration of a power control strategy for the generating set illustrated



in Figures 7A and 7B when operated at maximum engine speed; and

Figures 9A and 9B together comprise a circuit diagram of an AC power generating system according to another embodiment of this invention, the junction between the two parts of the circuit diagram being shown by a chain dotted line in each of Figures 9A and 9B.

Figures 1A and 1B show the components of an electrical power generating set in diagrammatic form. The generating set includes a Diesel Engine EN which is mechanically coupled with a rotor of a permanent magnet power generator GS whereby to rotate the rotor relative to a stator of the generator GS and thereby to generate a three-phase variable frequency and voltage AC output from the generator GS.

The engine EN has an electronic speed sensor SS, a speed controller SC and an electronically controlled fuel injection system. The speed sensor SS produces an output signal SVas which is indicative of the sensed engine speed. The speed controller SC has four input ports. One of those input ports receives a minimum speed reference signal SRms. Another of those input ports receives the actual speed signal SVas which is emitted by the speed sensor SS. A third of the input ports of the speed controller SC receives a speed demand signal SVsd and the fourth input port receives a speed correction signal SVsc. The signals SVsd and SVsc received by the latter two of the input ports of the speed controller SC are generated by operation of the generating set as is described below. The speed controller SC controls operation of the fuel injection system of the engine EN in response to those four input signals in order to control the speed of the engine EN and maintain it above the minimum speed and at the demanded level which is related to the sum of the three signals SRms, SVsd and SVsc.

The three phase variable frequency and voltage output of the generator GS is rectified by a full wave bridge rectifier Re which has a pair of output terminals.

A voltage booster circuit BO is connected across the output terminals of the bridge rectifier Re. The booster circuit BO includes a filter comprising an inductor Lf and a capacitor Cf. One side of the inductor Lf is connected to the positive output terminal of the bridge rectifier Re. The other side of the inductor Lf is connected to one side of the capacitor Cf and to one side of another inductor Lba. The other side of the capacitor Cf is connected to the negative output terminal of the bridge rectifier Re. The output voltage across the capacitor Cf is a filtered version of the output voltage from the bridge rectifier Re. The other side of the other inductor Lba is connected to the collector of a transistor Tb and to the anode of a diode Db. A third inductor Lbb is connected in series between the negative output terminal of the bridge rectifier Re and a current sensor CSB. The other side of the current sensor CSB is connected to the emitter of the transistor Tb and to one side of

a capacitor  $C_b$ . The cathode of the diode  $D_b$  is connected to the other side of the capacitor  $C_b$  so that current normally flows to the capacitor  $C_b$ . The voltage across the capacitor  $C_b$  is boosted by the combined effect of the inductors  $L_{ba}$  and  $L_{bb}$  with the switching action of the parallel connected transistor  $T_b$  and the series diode  $D_b$ . The output of the booster circuit BO is the voltage across the capacitor  $C_b$  which is maintained substantially constant and forms the DC link voltage.

A voltage sensor  $V_{Sa}$  is connected across the capacitor  $C_b$  and thereby is operable to monitor the DC link voltage. The voltage sensor  $V_{Sa}$  emits an output signal  $SV_{dc}$  which is an indication of the DC link voltage at any one instant. The current sensor  $CSB$  emits an output signal  $Sldg$ . A second current sensor  $CSL$  measures current flow through an intermediate DC link inductor  $L_{dc}$  which has one side connected to the positive output terminal of the booster circuit BO which is connected to the cathode of the diode  $D_b$  and to the capacitor  $C_b$  across which the DC link voltage is maintained. The second current sensor  $CSL$  emits a signal  $Slda$ .

A voltage controller  $VCM$  has three input ports. Another voltage controller  $VCT$  has two input ports. Each of the voltage controllers  $VCM$  and  $VCT$  has one output. One of the input ports of each of the voltage controllers  $VCM$  and  $VCT$  is connected to the output of the voltage sensor  $V_{Sa}$ . Another input port of the voltage controller  $VCM$  and the other input port of the voltage controller  $VCT$  respectively receives a respective reference signal  $SRVC$ ,  $SRIC$ .

A current controller  $CC$  has its output connected to the base of the transistor  $T_b$  and has four input ports. The current controller  $CC$  is designed to act on the higher of the signals received at two of its input ports A and B. The output of each of the voltage controllers  $VCM$  and  $VCT$  is connected to a respective one A, B of the two input ports A and B of the current controller  $CC$ . One of the other two input ports of the current controller  $CC$  is connected to the output of the current sensor  $CSB$  so as to receive the signal  $Sldg$ . The fourth input port of the current controller  $CC$  receives a signal  $SILc$  from the output of a maximum load torque correction controller  $MTC$ . The controller  $MTC$  provides a profile of available, or permissible generator currents at different speeds and loading conditions, and operates to select the appropriate one as the output signal  $SILc$ . The controller  $MTC$  has four input ports.

One of the input ports of the controller  $MTC$  receives the signal  $Sldg$  from the current sensor  $CSB$  of the booster circuit BO. Another of the input ports of the controller  $MTC$  receives the actual speed signal  $SVas$  produced by the engine speed sensor  $SS$ . A third of the input ports of the controller  $MTC$  receives the signal  $Slda$  from the other current sensor  $CSL$  which measures the DC link current. The fourth input port of the controller

MTC receives the output signal SVdc of the voltage sensor VSa which monitors the DC link voltage that is maintained across the capacitor Cb.

A speed correction circuit SCC has two inputs and an output from which the speed correction signal SVsc is fed to the speed controller SC. One of the inputs of the speed  
5 correction circuit SCC receives the current signal Slda from the current sensor CSL. The other input of the speed correction circuit SCC receives a reference signal SRLC.

The current signal Slda from the current sensor CSL is also fed to a voltage correction circuit VC which has a single output. The signal SViL emitted by the voltage correction circuit VC is fed to the third input of the voltage controller VCM. A regulator  
10 circuit RV which generates the signal SVsd has two inputs. One of those inputs receives the signal SVdc from the voltage sensor VSa that monitors the DC link voltage across the capacitor Cb. The other of those inputs receives a reference signal SRSD.

A brake controller BC is connected across the DC link voltage. The brake controller BC includes a resistor Rh in series with a transistor Th, the resistor Rh being connected to  
15 the collector of the transistor Th. A reverse biased diode DH is connected in parallel with the resistor Rh. The base of the transistor Th is connected to the output of a brake voltage controller BVC which has two inputs. One of those inputs is connected to the output of the voltage sensor VSa so as to receive the signal SVdc. A reference signal SRBC is applied to the other input of the brake voltage controller BVC.

20 A three phase DC/AC inverter INV has a power output terminal U,V,W for each phase and a neutral terminal N. The inverter INV is connected across the DC link voltage. The inverter INV includes a pair of capacitors Cna and Cnb which are connected in series across the DC link voltage. One end terminal of an inductor Ln is connected to the series connection between the two capacitors Cna and Cnb which is also connected to the neutral  
25 terminal N by a neutral line. The other terminal of the inductor Ln is connected through a current sensor CSN to the emitter of one transistor Tna and to the collector of another transistor Tnb. The two transistors Tna and Tnb are connected in series across the DC link voltage.

The AC power output for each phase of the three phase output of the inverter  
30 INV is produced by operation of a respective one of three other pairs of transistors Tua and Tub; Tva and Tvb; Twa and Twb which are connected in parallel with one another and with the pair of transistors Tna and Tnb. The transistors of each pair Tua and Tub; Tva and Tvb; Twa and Twb are series connected by having the emitter of one of that pair connected to the collector of the other of that pair, the series connection between the transistors of each pair  
35 Tua and Tub, Tva and Tvb, Twa and Twb being connected to the respective power output terminal U,V,W through a respective inductor Lu, Lv, Lw. Each of the inductors Lu, Lv

and  $L_w$  is the inductor of an LC filter which is provided for each phase. The capacitor  $C_u$ ,  $C_v$ ,  $C_w$  of each LC filter is connected between the output line for the respective phase and the neutral line through a respective current sensor  $CS_u$ ,  $CS_v$  and  $CS_w$ .

Each of the transistors  $T_b$ ,  $T_a$ ,  $T_{na}$ ,  $T_{nb}$ ,  $T_{ua}$ ,  $T_{ub}$ ,  $T_{va}$ ,  $T_{vb}$ ,  $T_{wa}$  and  $T_{wb}$  is an insulated Gate Bipolar Transistor (IGBT) and has an inverse parallel diode connected across its emitter and collector junctions (diode cathode to collector, diode anode to emitter). Other forms of transistor could be used. Each of the current sensors  $CSB$ ,  $CSL$  and  $CSN$  is a DC current transducer.

A voltage sensor  $VS_n$  senses the instantaneous division of voltage between the pair of capacitors  $C_{na}$  and  $C_{nb}$  that are connected across the DC link voltage. The voltage sensor  $VS_n$  has three inputs and two outputs. A first of those inputs is connected to the positive terminal of the capacitor  $C_{na}$ . A second of those inputs is connected to the negative terminal of the capacitor  $C_{nb}$ . The third input is connected to the neutral terminal  $N$ . The voltage sensor  $VS_n$  has two outputs, each of which is connected to a respective input of a voltage equaliser  $Ve$ . The output of the current sensor  $CSN$  is connected to a third input of the voltage equaliser  $Ve$ . Each output of the voltage equaliser  $Ve$  is connected to the base of a respective one of the transistors  $T_{na}$  and  $T_{nb}$ . The voltage equaliser  $Ve$  responds to the sensed division of voltage across the capacitors  $C_{na}$  and  $C_{nb}$  and the sensed current which has been smoothed by the inductor  $L_n$  by controlling operation of the two transistors  $T_{na}$  and  $T_{nb}$  as switches so as to control the neutral point of the AC output voltage of each phase and thereby to maintain symmetry of the output voltage under un-balanced load conditions.

The reference levels for the required value of output frequency and voltage are provided by an Amplitude and Frequency Correction circuit AFC which has eight inputs and three outputs. The output signal  $SV_{dc}$  from the voltage sensor  $VS_a$ , the actual sensed speed signal  $SV_{as}$  from the speed sensor  $SS$  and the output  $S_{lda}$  from the current sensor  $CSL$  that senses the DC link current are each connected to a respective one of the inputs of the Amplitude and Frequency Correction circuit AFC. Reference signals  $SR_{UI}$ ,  $SR_{VI}$ ,  $SR_{WI}$ ,  $SR_{IF}$  and  $SR_{RF}$  are fed to respective ones of the five other inputs to the Amplitude and Frequency Correction circuit AFC. The output signals emitted from the three outputs of the Amplitude and Frequency Correction circuit AFC are  $SV_{w2}$ ,  $SV_{v2}$  and  $SV_{u2}$ .

A voltage sensor  $VS_{uvw}$  senses the output voltage of each of the AC power outputs for each phase  $U$ ,  $V$  and  $W$  with respect to the neutral line  $N$ . The voltage sensor  $VS_{uvw}$  has three outputs which are fed to respective inputs of an Inverter controller IC which also receives the three output signals  $SV_{u2}$ ,  $SV_{v2}$  and  $SV_{w2}$  from the Amplitude and Frequency Correction circuit AFC at respective ones of three other inputs and the three output signals

from the current sensors CSu, CSv and CSw at respective ones of three further inputs.

The inverter controller IC uses the capacitor currents measured by the current sensors CSu, CSv and CSw to produce a phase lead characteristic for fast response and optimised damping. The Inverter controller IC has six outputs Tua, Tub, Tva, Tvb, Twa and Twb which are each connected to the base of a respective one of the output transistors Tua, Tub, Tva, Tvb, Twa and Twb. The inverter controller IC thereby operates to control operation of the transistors Tua, Tub, Tva, Tvb, Twa and Twb of the inverter INV by pulse width modulation.

The DC/AC pulse width modulated inverter INV produces a sinusoidal three phase voltage for symmetrical and non-symmetrical loads. The output voltage of each phase U,V,W is individually modulated by the Inverter Controller IC and the neutral point is controlled by the voltage equalizer Ve to maintain optimum symmetry of the output voltage under un-balanced load conditions.

At no load, steady state operation of the generating set, the engine EN will be controlled at minimum speed as set by reference SRms on the Speed Controller SC. The control system will hold the DC link voltage across the capacitor Cb constant. This is achieved by operation of the voltage controller VCM, the signal reference SRVC, the signal voltage SViL from the Voltage Correction Circuit VC and the DC link voltage feedback signal SVdc. Any difference between the sum of the reference signal SRVC and the signal voltage SViL on the one hand and the feedback signal SVdc on the other hand will produce an error voltage on the output from the voltage controller VCM. The output from the voltage controller VCM provides the demand signal Sldgr1 for the input A of the current controller CC. The current controller CC acts on that signal Sldgr1 and ignores the output Sldgr2 from the voltage controller VCT if the latter is low because SVdc>SRIC. The feedback signal for the current controller CC is Sldg from the current sensor CSB. The output from the current controller CC provides a pulse width modulated drive to the booster transistor Tb which sources a controlled current to maintain the voltage across the capacitor Cb constant, as regulated by the voltage controller VCM.

The output SVsc of the speed correction circuit SCC will increase speed demand in a proportional manner whenever the feedback signal Slda from the current sensor CSL exceeds the reference signal SRLC. This speed compensation is included to avoid a 'Dead Corner' or flat response area of the control system when high current is demanded at minimum speed. The time taken to produce the reference signal SVsc when Slda>SRLC is short which is advantageous because the engine speed increases more quickly in reaction to the demand for high current at low speed than would the feedback speed demand signal SVsd that is produced in response to the consequent fall in the intermediate load voltage across the

capacitor  $C_b$ . This is because the load current increases quickly in response to the demand for high current at low speed so that the speed correction signal  $SV_{sc}$  does likewise whereas the voltage across the capacitor  $C_b$  falls slowly so that the signal  $SV_{sd}$  falls slowly as well which results in a slow rise in the reference signal  $SV_{sd}$ .

5           A proportional increase in the intermediate DC link voltage is provided by the Voltage Correction circuit VC by adding an additional demand proportional to the DC link current, namely the output signal  $SV_{iL}$  that is fed to the voltage controller VCM. This improves the ability of the inverter INV to maintain the output voltage at a higher level than the reference level  $SRVC$  at high loads.

10           As load is applied to the output of the inverter INV, the DC link voltage across the capacitor  $C_b$  will vary transiently before corrective action is taken. No such corrective action is needed or taken during normal load operating conditions. The amount of voltage variation that occurs due to abnormal loading conditions determines which of the various electronic controllers of the generating set operate. Figure 2 shows that the DC signal  
15           reference levels for the various controllers are in the following order, starting with  $SRVC$  (Voltage control), the no-load reference level for voltage control of the DC link, each control being brought into operation when its signal reference level is reached during operation of the generating set. The exact settings of the reference levels is determined during tuning and optimization of the control system of the generating set.

20           If the DC Link voltage increases  $SRBC$  (Brake Controller) is the first control to be brought into operation and it does so to apply a controlled resistive load to the DC link if  $SV_{dc} > SRBC$ .  $SRIF$  (Increase Frequency) is brought into operation, to increase the frequency of the output of the inverter INV if  $SV_{dc} > SRIF$ .

$SRSD$  (Speed demand), is the first control to be brought into operation if the DC link  
25           voltage is reduced and it does so to increase speed of the engine EN if  $SV_{dc} < SRSD$ .  $SRIC$  (Increase current) is brought into operation for a short time to use kinetic energy to increase current from the generator GS if  $SV_{dc} < SRIC$  and if the feedback signal  $S_{lda}$  from the current sensor CSL exceeds the reference signal  $SR_{LC}$ .  $SRRF$  (Reduce frequency) is brought into operation, to reduce the frequency and voltage of the inverter output if  
30            $SV_{dc} < SRRF$ .

            When a step load is applied to the output of the inverter INV, the voltage across the capacitor  $C_b$  will vary transiently from its controlled constant value. The DC link voltage feedback signal  $SV_{dc}$  decreases and the output from the voltage controller VCM, through operation of the current controller CC, will demand more current to maintain the voltage  
35           across the capacitor  $C_b$  constant. The current limit (or maximum value) demanded by the current controller CC is determined by the maximum load torque correction circuit MTC

which provides the appropriate output signal SILc from its profile of available, or permissible generator currents at different speeds and loading conditions.

If the applied step load causes a current limit condition in the current controller CC, the DC link feedback voltage SVdc will fall below the reference level SRSD. The voltage regulator RV will operate as well as the voltage controller VCM. The output SVsd from the regulator Rv will actuate the speed controller SC to demand an increase in engine speed which will eventually result in a greater voltage output from the generator GS. The combined action of the voltage controller VCM, the voltage correction circuit VC, the speed correction circuit SCC, the regulator RV and the speed controller SC will restore the DC link voltage across the capacitor Cb.

If the applied step load causes the feedback voltage signal SVdc to fall below the reference signal SRIC, the voltage controller VCT will also operate to produce a higher output Sldgr2 than the output Sldgr1 from the voltage controller VCM. Signal Sldgr 2, which will be the difference between the signals SRIC and SVdc, will be produced and will cause an increased demand for current from the generator GS. That increase will be supplied mainly by kinetic energy from the rotating parts of the generator GS. The speed correction circuit SCC will operate to increase a speed demand. The regulator RV will also operate to provide increased speed demand. Again the combined action of the controllers will restore the voltage across the capacitor Cb.

If the applied step load caused the feedback voltage SVdc falls below the reference level SRRF, the Amplitude and Frequency Correction circuit AFC will also operate. This produces a reduction in the voltage and frequency of the output of the inverter INV to provide a transient off loading effect which will restore the voltage across the capacitor Cb.

The brake controller BC is used when the generating set is operated as a regenerative system. If the feedback signal voltage SVdc increases sufficiently to exceed the reference signal SRBC and so as to produce a higher output Sldgr2 from the Voltage Controller VCT for a given period of time, the Brake controller BVC will operate and produce a pulse width modulating drive to the transistor Th whereby to dissipate regenerative energy in the resistor Rh, which will restore the voltage across the capacitor Cb.

If the feedback voltage SVdc increases sufficiently to exceed the reference signal SRIF, the Amplitude and Frequency Correction circuit AFC will operate to produce an increase in the inverter output frequency to reduce the regenerative effect of motor type loads.

Figure 3 shows a modification which adds a Reservoir Capacitor RC1 with an associated charging and discharging system and smoothing components to the adjustable speed generating set shown in Figures 1A and 1B in order to provide that generating set

with capacitor support. An extra power diode  $D_s$  is added in series with the DC link between the junction of the inductor  $L_{bb}$  and the emitter of the transistor  $T_b$  with the anode of the diode  $D_s$  facing the emitter of the transistor  $T_b$ . Under normal conditions the diode  $D_s$  is forward biased as the DC link current flows from the positive to the negative of the rectifier  $R_e$ .

The charging and discharging system for the Reservoir Capacitor  $RC1$  has an output terminal which is connected between the diode  $D_s$  and the inductor  $L_{bb}$ , and positive and negative input terminals which are connected in parallel with DC link at appropriate locations.

One side of the Reservoir Capacitor  $RC1$  is connected to the negative input terminal of the charging and discharging circuit, to the anode of a diode  $D_{cs2}$  and to one side of a capacitor  $C_{cs1}$ . The other side of the Reservoir Capacitor  $RC1$  is connected in parallel with a series connected resistor  $R_{cs2}$  and capacitor  $C_{cs3}$ , with another diode  $D_{cs3}$  and with a series connected inductor  $L_{cs2}$  and resistor  $R_{cs1}$ . The cathode of the diode  $D_{cs2}$  is connected to the emitter of a transistor  $T_{cs}$  which has its collector connected to the emitter of another transistor  $T_{cc}$  which in turn has its collector connected to the positive input terminal of the charging and discharging circuit. The connection between the two transistors  $T_{cc}$  and  $T_{cs}$  is connected in parallel with the cathode of the diode  $D_{cs3}$ , the capacitor  $C_{cs3}$  and the other end of the series connected inductor  $L_{cs2}$  and resistor  $R_{cs1}$ . The other side of the capacitor  $C_{cs1}$  is connected to the diode  $D_{cs1}$  which has its cathode connected to the output terminal of the charging and discharging circuit. The other side of the capacitor  $C_{cs1}$  is also connected to the emitter of the transistor  $T_{cs}$  through an inductor  $L_{cs1}$ .

A capacitor support control circuit  $CS$  has four inputs and two outputs. One of the inputs receives the DC Link voltage signal  $SV_{dc}$  from the voltage sensor  $V_{sa}$ . Each of the other three inputs receives a respective reference signal  $SRECC$ ,  $SRSCC$  or  $SRCS$ . Each of the outputs is connected to the base of a respective one of the transistors  $T_{cc}$  and  $T_{cs}$  to provide those transistors with pulse width modulated control so that they function as switches.

The Reservoir Capacitor  $RC1$  is charged from the DC Link portion of the main control circuit under pulse width control provided by a buck converter formed by the transistor  $T_{cc}$ , the diode  $D_{cs3}$ , the inductor  $L_{cs2}$  and the resistor  $R_{cs1}$ , the resistor  $R_{cs2}$  and the capacitor  $C_{cs3}$  suppressing diode switching noise. Discharge of the Reservoir Capacitor  $RC1$  is under pulse width modulated control provided by a buck converter formed by the transistor  $T_{cs}$ , the diode  $D_{cs2}$ , the inductor  $L_{cs1}$  and the capacitor  $C_{cs1}$  and the electrical energy so discharged is injected in series into the DC Link portion of the main control circuit through the diode  $D_{cs1}$ , the power diode  $D_s$  being reverse biased under those



conditions.

The DC signal reference levels for the various controllers of the control circuit modified as shown in Figure 3 differ from those described above for the main control circuit with reference to Figures 1A, 1B and 2 by the addition of a first reference SRECC (Emergency Charge Capacitor) which effects charging of the Reservoir Capacitor RC1, ignoring the state of that charge if the DC Link voltage  $SV_{dc} > SRECC$ ; and by the insertion of two other references, one, SRSCC immediately before the reference level SRIC(Increase current) and the other, SRCS immediately after that reference SRIC. This modified control strategy is illustrated in Figure 4. The reference SRSCC (Stop Charging Capacitor) halts charging of the Reservoir Capacitor RC1 if the DC Link voltage  $SV_{dc} < SRSCC$ . The reference SRCS (Capacitor Support) effects energy discharge from the Reservoir Capacitor RC1 if the DC Link voltage  $SV_{dc} < SRCS$ .

Hence, if the applied step load causes the DC Link voltage  $SV_{dc}$  to fall below the reference level SRSCC, the charging of the Reservoir Capacitor RC1 will stop. Depending on the state of charging of the Reservoir Capacitor RC1 at the time, this would release a surge of power and increase the DC Link voltage  $SV_{dc}$ . On the other hand, if the DC Link feedback voltage  $SV_{dc}$  falls below the reference level SRCS, electrical energy from the Reservoir Capacitor RC1 will be injected in series into the intermediate DC link portion of the main control circuit in a controlled manner. Since the main control circuit described above with reference to Figures 1A and 1B is a regenerative system which includes the brake controller BC, if the DC Link voltage  $SV_{dc}$  should increase sufficiently to exceed the first reference level SRECC, emergency charging of the Reservoir Capacitor RC1 would begin in an attempt to absorb the re-generative energy in a limited manner.

Figure 5 shows another modification which adds a battery pack BP1 and an associated charging and discharging system with smoothing components instead of the Reservoir Capacitor RC1 and its associated charging and discharging system with smoothing components. The elements of the charging and discharging system shown Figure 5 which are similar to those of the similar system described above with reference to Figure 3 are identified by the same references except that the character 'c' is replaced by the character 'b'.

The charging and discharging system shown in Figure 5 differs from that shown in Figure 3 in that there is no resistor equivalent to the resistor Rcs2 in series with the capacitor Cbs2. On the other hand there is an additional inductor Lbs3 which is connected in series between the emitter of the transistor Tbc and the collector of the transistor Tbs. The battery support control circuit BS has two more inputs than the capacitor support control circuit CS. One of those extra inputs receives a current signal from a current sensor CSbp

which measures the current between the junction of the inductor Lbs3 and the collector of the transistor Tbs and the junction of the inductor Lbs2 and the capacitor Cbs2. The other extra input of the battery support control circuit BS receives a voltage signal from a voltage sensor VSb which is connected across the terminals of the battery pack BP1.

5           The buck convertor for charging the battery pack BP1 is formed by the transistor Tbc, the diode Dbs3, the inductor Lbs3 and the capacitor Cbs2. The inductor Lbs2 provides further battery ripple smoothing. Control of discharging of the battery pack BP1 is similar to that described above for the Reservoir Capacitor RC1.

          The DC signal reference levels for the various controllers of the control circuit  
10   modified as shown in Figure 5 are similar to those that have described above with reference to Figures 3 and 4, the additional references being SRECB (emergency charge battery), SRSCB (stop charging battery) and SRBS (Battery support) rather than SRECC, SRSCC and SRCS as described for the Reservoir Capacitor RC1. The modified control strategy is illustrated in Figure 6. Operation of the modified control circuit shown in Figure 5 will be  
15   understood from the foregoing description of the modified control circuit shown in Figure 3.

Figures 7A and 7B show the components of an electrical power generating set which differs from that shown in Figures 1A and 1B in a number of respects.

          Firstly the engine speed control is simplified. There is no speed demand signal SVsd and the RV regulator of the circuit shown in Figure 1A is omitted. The speed correction  
20   signal is retained. It is identified as Srs in Figure 7A and there is an additional maximum speed reference signal Srmax. The output signal produced by the speed sensor SS is identified as Ssp in Figure 7A. The speed controller SC controls operation of a fuel injection system of the engine EN in response to the four input signals Ssp, the speed correction signal Srs and the minimum and maximum speed reference signals Srmin and  
25   Srmax in order to control the engine EN and to maintain the speed of the engine EN between the minimum and maximum speeds and at a level which is related to the speed correction signal Srs which is derived from the intermediate load current.

          The second difference is that the permanent magnet power generator G has two three-phase variable frequency and voltage outputs Ga and Gb.

30           Thirdly, each of the two three-phase variable frequency and voltage outputs Ga and Gb is rectified by a respective one of two full wave bridge rectifiers Re3a and Re3b. Each bridge rectifier Re3a, Re3b has a connection to a neutral terminal and has a power output terminal which, with respect to the neutral is positive in the case of the bridge rectifier Re3a and negative in the case of the bridge rectifier Re3b.

35           Another difference is that there are two booster circuits in the generating set shown in Figures 7A and 7B. A respective one of the two booster circuits is connected across the

terminals of each bridge rectifier Re3a and Re3b and is operable to boost the power output voltage of the respective bridge rectifier Re3a, Re3b.

A further difference is that the neutral N of the AC power output of the generating set shown in Figures 7A and 7B is connected to the common neutral terminal of the two bridge rectifiers Re3a and Re3b and of the two booster circuits. This is in contrast to the arrangement of the electrical power generating set which is described above with reference to Figures 1A and 1B in which the neutral of the AC power output is generated and regulated by a controlled division of the intermediate DC voltage. The common neutral of the two bridge rectifiers Re3a and Re3b and of the two booster circuits is preferred in the arrangement of the electrical power generating set shown in Figures 7A and 7B because it is a reliably constant potential which serves as a good reference potential.

A further difference is that the DC link voltages generated by the two booster circuits of the generating set shown in Figures 7A and 7B, that comprise the potential drops Va and Vb across the two pairs of capacitors Ca1 and Ca2, and Cb1 and Cb2 that are connected in parallel, are monitored independently. Also the switching action of each of the transistors Ta and Tb of the two booster circuits is controlled independently by a respective separate control circuit. It is this independent control of the switching action of the transistors Ta and Tb that leads to the common neutral being a reliably constant potential.

Each of these control circuits includes a combined voltage sensor Vsab which monitors the two DC link voltages Va and Vb and which produces a respective voltage output signal SVa, SVb which is an indication of the respective DC link voltage Va, Vb at any one instant; a voltage controller Vca, Vcb which compares the respective voltage output signal SVa, SVb with a respective reference signal SVar, SVbr to produce a basic current demand signal, which also compares the speed sensor output signal Ssp with a respective reference signal St and which adds any amount by which the speed sensor output signal Ssp exceeds the respective reference signal St to the respective basic current demand signal to produce a current demand signal SImxa, SImxb; and a current controller Cca, Ccb which receives the current demand signal SImxa, SImxb at one input and the output SIa, SIb of a respective current sensor CSIa, CSIb as a feedback signal at another input and which has its output connected to the base of the respective transistor Ta, Tb. The current sensor CSIa monitors the rectified current that flows from the positive output terminal of the full-wave rectifier Re3a to the collector of the transistor Ta. The current sensor CSIb monitors the current that flows from the emitter of the transistor Tb to the negative output terminal of the full-wave rectifier Re3b. The output from each current controller Cca, Ccb provides a pulse width modulated drive to the base of the respective transistor Ta, Tb which controls the current and voltages to maintain the voltage Va, Vb across the respective capacitors Ca1 and

Ca2, Cb1 and Cb2 constant as regulated by the respective voltage controller Vca, Vcb. Hence the intermediate DC voltage is maintained at the required potential by controlling that DC current output from the two rectifiers Re3a and Re3b.

There is no Maximum Load Torque Correction circuit in the generating set shown in  
 5 Figures 7A and 7B. This follows from the only engine speed correction being derived from the sensed intermediate load current.

In contrast to the control circuitry for the inverter INV of the electrical power generating set shown in Figures 1A and 1B in which the inverter controller IC uses the capacitor currents measured by current sensors CSu, CSv and CSw of the respective LC  
 10 filters for each phase of the three-phase output of the inverter INV to produce the phase lead characteristic for fast response and optimised damping, the inverter control circuitry of the electrical power generating set shown in Figures 7A and 7B has a current sensor CSIu, CSIv, CSIw connected between each of the junctions between the transistors Tau and Tbu, Tav and Tbv, Taw and Tbw of the inverter IN and the inductor Lu, Lv, Lw of the respective  
 15 LC filter for each phase of the three-phase output of the inverter IN. The output signals from the current sensors CSIu, CSIv, CSIw are fed to respective inputs of the Inverter Controller IC.

Figures 7A and 7B show the diesel engine EN which is mechanically coupled with the rotor of the permanent magnet power generator G whereby to rotate the rotor relative to  
 20 the stator of the generator G. The stator carries dual three phase windings Ga and Gb. Hence rotation of the rotor relative to the stator generates the two three-phase variable frequency and voltage AC outputs Ga and Gb from the generator G. The output Ga is positive and the output Gb is negative with respect to the neutral N.

The engine EN has an electronic speed sensor SS, the speed controller SC and the  
 25 fuel injection system. The speed sensor SS produces the output signal Ssp which is indicative of the engine speed.

The positive three-phase output Ga is connected to the rectifier Re3a and the negative three-phase output voltage Gb is connected to the rectifier Re3b.

Each booster circuit includes an inductor La, Lb which has one side connected to the  
 30 respective positive or negative output terminal of the respective bridge rectifier Re3a, Re3b. The other side of the inductor La, Lb is connected to one side of the respective current sensor CSIa, CSIb. The other side of the current sensor CSIa is connected to the collector of the transistor Ta and to the anode of a diode Da. The other side of the current sensor CSIb is connected to the emitter of the transistor Tb and to the cathode of a diode Db. The  
 35 emitter of the transistor Ta and collector of the transistor Tb are connected to the neutral line N. The cathode of the diode Da is connected to one side of each of a pair of capacitors Ca1

and Ca2. The anode of the diode Db is connected to one side of capacitor Cb1 and, through a third current sensor CSI, to one side of capacitor Cb2. The other side of each of the capacitors Ca1, Ca2, Cb1 and Cb2 is connected to the neutral line N. Hence current normally flows to the capacitors Ca1 and Ca2 and from the capacitors Cb1 and Cb2. The voltage across the capacitors Ca1 and Ca2 is boosted by the combined effect of the inductor La and the switching action of the parallel connected transistor Ta and the series diode Da. The voltage across the capacitors Cb1 and Cb2 is boosted by the combined effect of the inductor Lb and the switching action of the parallel connected transistor Tb and the diode Db. The output of the booster circuits is the voltage across the capacitors Ca1 and Cb1 and the sum of those voltages is maintained constant and forms the DC link voltage which is connected across the capacitors Ca2 and Cb2.

The voltage sensor Vsab is connected across the capacitor Ca1 and thereby is operable to monitor the voltage across that capacitor Ca1. Further the voltage sensor Vsab is also connected across the capacitor Cb1 and thereby is operable to monitor the voltage across that capacitor Cb1. Hence the voltage sensor Vsab is operable to monitor the DC link voltage. The voltage sensor VSab emits the two output signals SVa and SVb which are respectively an indication of the voltage across the capacitor Ca1 and the capacitor Cb1.

Each of the voltage controllers Vca and Vcb has four input ports and receives at one of those input ports the respective one of the output signals SVa and SVb emitted by the voltage sensor VSab. Each voltage controller Vca, Vcb receives the respective reference signal SVar, SVbr at another of its input ports. The output signal Ssp from the speed sensor SS is fed to a third of the input ports of each voltage controller Vca, Vcb and the reference signal St is fed to the fourth input port. The reference St is the maximum permitted current. It translates to a limiting speed for a given steady state power demand since current is varied by varying engine speed. The reference St is chosen as being the current that flows when the engine EN is operated at a certain working speed which is between the selected maximum and minimum engine speeds S<sub>rmax</sub> and S<sub>rmis</sub> and when a load is connected across the intermediate DC voltage. Each voltage controller Vca, Vcb has one output which emits the respective current demand signal SImxa, SImxb which is fed to an input of the respective current controller Cca, Ccb. Each current controller Cca, Ccb has another input at which the respective signal SIa, SIb from the respective current sensor CSIa, CSIb is received. Each current controller Cca, Ccb has one output which is connected to the base of the respective transistor Ta, Tb whereby to control the switching action of that transistor Ta, Tb.

A brake controller BC is connected across the DC link voltage between the parallel connected pairs of capacitors Ca1 and Cb1, Ca2 and Cb2. The arrangement and operation

of the brake controller BC is substantially the same as that of the adjustable speed electrical power generating set described with reference to Figures 1A and 1B.

The three-phase DC/AC inverter IN is connected across the brake controller BC.

The third current sensor CSI measures current flow between the brake controller BC  
 5 and the capacitors Cb1 and Cb2. The current sensor CSI emits a signal Sldb which is fed to one of the two inputs of the speed correction circuit SCC. The other input of the speed correction circuit SCC receives a reference signal SrLC and the output of the speed correction circuit SCC is the signal Srs that is fed to the speed controller, SC.

As with the electrical power generating set described above with reference to Figures  
 10 1A and 1B, the AC power output for each phase of the three-phase output of the inverter IN is produced by operation as bistable switching means of a respective one of three pairs of transistors Tau and Tbu; Tav and Tbv; Taw and Tbw which are connected in parallel with one another and with the pair of capacitors Cb1 and Cb2. Again, like the inverter INV of the adjustable speed generating set shown in Figures 1A and 1B, the series connection  
 15 between the transistors of each pair Tau and Tbu, Tav and Tbv, Taw and Tbw is connected to the respective power output terminal U,V,W through a respective inductor Lu, Lv and Lw. As explained above the current sensor CSlu, CSlv and CSIw is connected between the respective inductor Lu, Lv and Lw and the series connections between the respective pair of transistors Tau and Tbu, Tav and Tbv, Taw and Tbw. The three current sensors CSlu, CSlv  
 20 and CSIw sense the current flow to the output terminals U,V,W and to the capacitors Cu, Cv and Cw of the respective LV filters. The output of each current sensor CSlu, CSlv and CSIw is connected to a respective input of the inverter controller IC.

The reference levels for the required values of output frequency and voltage are provided by an amplitude and frequency correction circuit AFC which has seven inputs and  
 25 three outputs. It receives the output signals SVa and SVb from the voltage sensor VSab, that indicate the voltages across the capacitors Ca1 and Cb1 which together comprise the DC link voltage, at respective ones of the inputs. At the remaining five inputs it receives respective reference signals SRIF, SRRF, Sru1, Srv1 and Srw1. Three outputs Sru2, Srv2 and Srw2 from the voltage, amplitude and frequency correction circuit AFC are fed to  
 30 respective one of three inputs of the inverter controller IC which also receives three input signals Svu, Svv and Svw from a voltage sensor Vsuvw which senses the output voltages at the respective power output terminals U,V,W and the neutral N of the generating set. The inverter controller IC controls the switching operation of the transistors Tau, Tbu, Tav, Tbv, Taw, Tvw of the three pairs of transistors of the inverter IN by emitting a respective pulse-  
 35 width modulation signal from a respective output Tau, Tbu; Tav, Tbv; Taw, Tbw which is connected to the base of the respective transistor Tau, Tbu, Tav, Tbv, Taw, Tbw.

Operation of the electrical power generating set shown in Figures 7A and 7B will be apparent from the foregoing description of operation of the adjustable speed electrical power generating set shown in Figures 1A and 1B in conjunction with the foregoing description of Figures 7A and 7B.

5 Figure 8 is a graph of Power against speed. Power is the product of Torque and speed. The upper curve is engine power. The lower curve is power produced by the generator G under normal conditions when its operation is controlled by the control system described above with reference to Figures 7A and 7B.

Controlling the current controls the torque. Hence limiting current limits torque.

10 The closer the engine EN is run to operating at maximum torque, the lower is the available acceleration. The engine EN cannot be accelerated when it is running at maximum speed.

The torque, and thus the power produced by the generator G is allowed to be increased up to the level of the maximum power that is delivered by the engine EN as the engine EN approaches its maximum speed.

If  $SSp > St$  then the reference current signal  $SI_{mxa}$  and  $SI_{mxb}$  are increased proportionately to the difference between  $St$  and  $SSp$ .

The torque produced by the generator G is allowed to be increased up to the maximum torque produced by the engine EN until a balance is achieved.

20 Figures 9A and 9B show the components of a generating set which is similar to that shown in Figures 7A and 7B in many respects. Like parts are identified in Figures 7A and 7B and 9A and 9B by the same reference characters and the foregoing description with reference to Figures 7A and 7B applies equally to Figures 9A and 9B in their respect. The differences between these two generating sets are in the inverter IN and are now described with reference to Figures 9A and 9B.

The junction between the pair of transistors  $T_{au}$  and  $T_{bu}$ ,  $T_{av}$  and  $T_{bv}$ ,  $T_{aw}$  and  $T_{bw}$  for each phase of the three phase inverter IN is also connected to one side of a respective rectifier  $Reu$ ,  $Rev$ ,  $Rew$ . The other side of the rectifier  $Reu$ ,  $Rev$ ,  $Rew$  is connected to the neutral line N. A third bistable switch for each phase, which is in the form of a transistor  $T_u$ ,  $T_v$ ,  $T_w$  is connected in parallel with the respective rectifier  $Reu$ ,  $Rev$ ,  $Rew$  with its emitter connected to the neutral input of the rectifier  $Reu$ ,  $Rev$ ,  $Rew$  through one of the diodes of that rectifier  $Reu$ ,  $Rev$ ,  $Rew$ . Each transistor  $T_u$ ,  $T_v$ ,  $T_w$  with its parallel connected rectifier  $Reu$ ,  $Rev$ ,  $Rew$  operates as a bi-directional switch whereby zero voltage is supplied to the output filter  $L_u$ ,  $C_u$ ;  $L_v$ ,  $C_v$ ;  $L_w$ ,  $C_w$  of the respective phase when the transistor  $T_u$ ,  $T_v$ ,  $T_w$  is switched to its conducting state. Operation of each transistor  $T_u$ ,  $T_v$ ,  $T_w$  is controlled by an appropriate pulse width modulation signal from the inverter

controller IC which is fed to the base of the respective transistor Tu, Tv, Tw through a respective output Tu, Tv, Tw of the inverter controller IC.

The current sensor CS<sub>Iu</sub>, CS<sub>Iv</sub> and CS<sub>Iw</sub> that is connected between the respective inductor Lu, Lv, Lw and the series connections between the respective pair of transistors  
 5 Tau and Tbu, Tav and Tbv, Taw and Tbw is also connected between the respective inductor Lu, Lv, Lw and a respective pair of the diodes of the respective rectifier Reu, Rev, Rew.

The controllers Vca, Vcb, Cca, Ccb, SCC, SC, AFC and IC are programmed microprocessors. The current sensors CS<sub>Ia</sub>, CS<sub>Ib</sub>, CS<sub>I</sub>, CS<sub>Iu</sub>, CS<sub>Iv</sub> and CS<sub>Iw</sub> sense current  
 10 flow in a wire into which they are connected. They may be galvanic, such as a shunt, non-galvanic in that they sense an electromagnetic field around the wire and emit an output signal based thereon, or they may be a current transformer.

Operation of the electrical power generating set shown in Figures 9A and 9B will be apparent from the foregoing description of operation of the electrical power generating set shown in Figures 1A and 1B in conjunction with the description of Figures 7A and 7B.

15 In operation, each of the transistors Tau, Tav, Taw of each phase of the inverter IN is switched to its operating state by a pulse width modulation signal applied to its base by the inverter controller IC. The other transistors Tbu, Tbv, Tbw and Tu, Tv, Tw are in their non-conducting state. Hence the transistor Tau, Tav, Taw connects the capacitor Ca2 to the respective output terminal U, V, W through the inductor Lu, Lv, Lw of the respective LC  
 20 output filter for that phase. The capacitor Ca2 is charged to the positive DC potential Va. After a short period of time in which the positive DC potential Va is fed to the output terminals, the transistor Tau, Tav, Taw is switched to its other, non-conducting state and the transistors Tu, Tv, Tw are switched to their conducting state in which they connect the side of the conductor Lu, Lv, Lw remote from the respective capacitor Cu, Cv, Cw to neutral so  
 25 that the voltage produced by the respective rectifier Reu, Rev, Rew with the respective transistor Tu, Tv, Tw is zero volts. The other transistors Tbu, Tbv, Tbw remain in their non-conducting state for this period but they are subsequently switched to their conducting state when the transistors Tu, Tv, Tw are switched to their non-conducting states and the transistors Tau, Tav and Taw remain in their non-conducting states. Switching the  
 30 transistors Tbu, Tbv, Tbw to their conducting state connects the negative DC potential stored in the capacitor Cb2 to the respective output terminal U, V, W through the respective inductor Lu, Lv, Lw for a further period. At the end of that period, the transistors Tbu, Tbv, Tbw are switched to their non-conducting state and the transistors Tu, Tv and Tw are again switched to their conducting states so that the voltage fed to the output terminals through the  
 35 inductors Lu, Lv, Lw is zero for a further period whilst the other transistors Tau, Tav, Taw remain in their non-conducting states. The sequence is then repeated. It will be understood



that the three phases are operated simultaneously but with a 120 degrees phase angle between them.

When the transistors Tu, Tv, Tw are switched to their conducting states to connect the output terminals to neutral, the sum of energy stored in the output filters Cu, Lu; Cv, Lv; Cw, Lw is constant, neglecting losses.

When a load is connected to the output terminals U, V, W and N there is no "reactive power flow" through the system to the DC potential capacitors Ca2 and Cb2.

The generating set described above with reference to Figures 9A and 9B has the following advantages. The ripple current is low. The intermediate DC voltage may be lower for the same output Vrms voltage. The capacitance of the DC link storage voltage may be lower. Switching losses may be lower and distributed. The transistors Tau, Tav, Taw, Tbu, Tbv, Tbw of the inverter IN are switched to their conducting state for shorter periods of time. The AC output filter capacity may be lower since the capacitor Cu, Cv, Cw would be charged by the lower ripple current. The modulation system voltage variability would be lower and would result in a stable operation so that lower rated capacitors and inductors may be used. That reduction in the rating of the output filter inductor Lu, Lv, Lw would yield a lower voltage drop when a non-linear load is applied. The rated voltage of the transistors Tu, Tv, Tw needs to be only half that of the main transistors Tau, Tav, Taw, Tbu, Tbv, Tbw.

Although the aspect of the invention that is illustrated in Figures 9A and 9B has been described as being carried out in a three phase system, it could be carried out in a single phase system or in a multiphase system having any suitable number of phases.

## CLAIMS

1. An AC power generating system including inverter means which are operable to convert an intermediate DC voltage into an AC power output, means for generating and  
5 maintaining the intermediate DC voltage at one level and voltage sensor means operable to monitor the intermediate DC voltage, characterised in that control means are provided for the inverter means, said control means being responsive to the voltage sensor means and being operable to control operation of the inverter means so the voltage and/or the frequency of the AC power output is/are reduced in response to a fall in the intermediate DC voltage to a  
10 certain level caused by the application of a step load to the AC power output, whereby to provide a transient off-loading effect which will give the generating means time to respond and thereby allow the intermediate DC voltage to be restored to said one level.
2. An AC power generating system according to claim 1 wherein a neutral setting of  
15 the AC power output is generated and regulated by a controlled division of the intermediate DC voltage.
3. An AC power generating system according to claim 1 or claim 2, wherein the means for generating and maintaining the intermediate DC voltage include generator means  
20 operable to generate a variable voltage AC power supply and rectifier means having an output and operable to rectify the variable voltage AC power supply to establish the intermediate DC voltage.
4. An AC power generating system according to claim 3 when appended to claim 2,  
25 wherein said rectifier means are full wave rectifier means.
5. An AC power generating system according to claim 3 or claim 4, including booster means having an input which is connected to the output of the rectifier means and which are operable to increase the voltage of the rectified output of said rectifier means and thereby to  
30 establish the intermediate DC voltage.
6. An AC power generating system according to claim 5, wherein the generator means include respective control means and the voltage sensor means provide a feedback control signal to the control means of the generator means whereby to effect variation of the  
35 variable AC power supply and thereby to counter a tendency of the intermediate DC voltage to vary.

7. An AC power generating system according to claim 5 or claim 6, wherein the generator means are driven by a variable speed prime mover, the control means of the generator means comprising speed control means operable to control the speed of the prime mover.

5

8. An AC power generating system according to claim 7, including current sensor means operable to monitor a DC load current caused by connection of a load across the intermediate DC voltage, the current sensor means being operable to emit a signal which is indicative of the monitored DC load current, comparator means being provided for  
10 comparing an output signal from the current sensor means with a reference signal and for emitting a speed correction signal which is proportional to the amount by which the signal from the current sensor means exceeds the reference, the output from the comparator means being supplied to the speed control means of the generator means to effect an increase in the speed of the prime mover above that required for the applied load.

15

9. An AC power generating system according to claim 6 or either of claims 7 and 8 when appended to claim 6, including brake control means and means responsive to the output of the voltage sensor means that is operable to monitor the intermediate DC voltage whereby to connect the brake control means across the intermediate DC voltage to apply a  
20 load thereto when the intermediate DC voltage rises to a certain level.

10. An AC power generating system according to claim 6, or claim 8, or either of claims 7 and 8 when appended to claim 6, wherein said control means for the inverter means are operable to increase the frequency of the AC power output in response to an increase in the  
25 intermediate DC voltage to a certain high level.

11. An AC power generating system according to claim 10 when appended to claim 9, wherein said certain high level is higher than the level at which the brake control means is connected across the intermediate DC voltage.

30

12. An AC power generating system according to claim 6 or to any one of claims 7 to 11 when appended to claim 6, including electrical energy storage means connected across the intermediate DC voltage so as to be charged by the intermediate DC voltage, wherein bistable switching means and associated control means are provided, said bistable switching  
35 means normally being in one state in which they are operable to make the connection of the electrical energy storage means across the intermediate DC voltage to enable the electrical

energy storage means to be charged and to interrupt a connection between the electrical energy storage means and the connection between the output of the rectifier means and the input of the booster means, said bistable switching means being operable in its other state to isolate the electrical energy storage means from the intermediate DC voltage and to make  
5 said connection of the electrical energy storage means with the connection between the output of the rectifier means and the input of the booster means whereby to enable discharge of electrical energy from said electrical energy storage means into the connection between the rectifier means and the booster means, said control means associated with the bistable switching means being responsive to the monitored intermediate DC voltage and being  
10 operable in response to a fall in said intermediate DC voltage to a certain reference level to switch said bistable switching means from its normal said one state to said other state whereby to discharge electrical energy to augment the rectified output of the said rectifier means and thereby to counter the sensed fall in said intermediate DC voltage.

15 13. An AC power generating system including generator means operable to generate a variable voltage AC power supply, rectifier means having an output and being operable to rectify the variable voltage AC power supply, booster means having an input which is connected to the output of the rectifier means and which are operable to increase the voltage of the rectified output of said rectifier means to establish an intermediate DC voltage, a  
20 variable speed prime mover drivingly coupled with the generator means, and speed control means operable to control the speed of the motor, wherein current sensor means are provided, said current sensor means being operable to monitor a current caused by connection of a load across the intermediate DC voltage, the current sensor means being operable to emit a signal which is indicative of the monitored current, comparator means  
25 being provided for comparing an output signal from the current sensor means with a reference signal and for emitting a speed correction signal which is proportional to the amount by which the output signal from the current sensor means exceeds the reference signal, the output from the comparator means being supplied to the speed control means for the generator means to effect an increase in the speed of the variable speed prime mover,  
30 characterised in that said current sensor means are operable to monitor the DC load current on the side of said booster means remote from said rectifier means..

14. An AC power generating system including generator means operable to generate a variable voltage AC power supply, rectifier means having an output and operable to rectify  
35 the variable voltage AC power supply, booster means having an input which is connected to the output of the rectifier means and which is operable to increase the voltage of the

rectified output of said rectifier means and thereby to provide an intermediate DC voltage, inverter means operable to convert the intermediate DC voltage into an AC power output for supplying to an external load, voltage sensor and control means operable to monitor the intermediate DC voltage and to provide a feedback control signal to the generator means  
5 whereby to vary the variable voltage AC power supply so as to counter a tendency of the intermediate DC voltage to vary, and electrical energy storage means connected across the intermediate DC voltage so as to be charged by the intermediate DC voltage, characterised in that bistable switching means and associated control means are provided, said bistable switching means normally being in one state in which they are operable to make the  
10 connection of the electrical energy storage means across the intermediate DC voltage to enable the electrical energy storage means to be charged and to interrupt a connection between the electrical energy storage means and a connection between the generator means and the input of the booster means, said bistable switching means being operable in its other state to isolate the electrical energy storage means from the intermediate DC voltage and to  
15 make said connection of the electrical energy storage means with the connection between the generator means and the input of the booster means, whereby to enable discharge of electrical energy from said electrical energy storage means into the connection between the generator means and the booster means, said control means associated with the bistable switching means being responsive to the monitored intermediate DC voltage and being  
20 operable in response to a fall in said intermediate DC voltage to a certain reference level to switch said bistable means from its normal said one state to said other state whereby to discharge electrical energy to augment the variable voltage AC power supply and thereby to counter the sensed fall in said intermediate DC voltage.

15. An AC power generating system according to claim 14, wherein said connection  
25 between the generator means and the input of the booster means is the connection between the rectifier means and the input of the booster means so that the discharged electrical energy augments the rectified output of said rectifier means.

16. An AC power generating system including variable voltage AC power supply  
30 generator means, rectifier means having an output and operable to rectify the variable voltage AC power supply to establish an intermediate DC voltage, inverter means which are operable to convert the intermediate DC voltage into an AC power output, voltage sensor means operable to monitor the intermediate DC voltage and control means responsive to an output from the voltage sensor means and operable to maintain the intermediate DC voltage  
35 at one level, characterized in that the generator means is operable to generate two variable voltage AC power supplies, the rectifier means comprise two full-wave rectifiers, each full-

wave rectifier being operable to rectify a respective one of the two variable voltage AC power supplies and each being connected on one side to a neutral terminal and having an output voltage terminal on its other side, the output voltage terminal of one of the full-wave rectifiers being positive and the output voltage terminal of the other full-wave rectifier being negative such that the two variable voltage AC power supplies are severally rectified to produce a positive and a negative potential respectively which together comprise the intermediate DC voltage, the voltage sensor means comprising two voltage sensors severally responsive to a respective one of the positive and negative output potentials of the two full-wave rectifiers and the control means comprising two controllers severally operable to maintain each of those positive and negative potentials at a certain level whereby to maintain the intermediate DC voltage at said one level.

17. An AC power generating system according to claim 16, wherein there are two booster circuits, each connected between a respective one of the output voltage terminals of the two full-wave rectifiers and the neutral terminal and each operable to increase the voltage of the respective output potential and thus to increase the intermediate DC voltage, each of the controllers being operatively associated with the respective one of the booster circuits.

18. An AC power generating system according to claim 16 or claim 17, wherein the generator means is a permanent magnet generator driven by a variable speed prime mover provided with speed control means operable to control the speed of the prime mover.

19. An AC power generating system according to claim 18, wherein current sensor means operable to monitor a DC load current caused by connection of a load across the intermediate DC voltage are provided, the current sensor means being operable to emit a signal which is indicative of the monitored DC load current, comparator means being provided for comparing an output signal from the current sensor means with a reference signal and for emitting a speed correction signal which is proportional to the amount by which the signal from the current sensor means exceeds the reference, that speed correction signal being supplied to the speed control means for the generator as a feed back control signal whereby to effect variation of the variable AC power supply and thereby to counter any tendency of the intermediate voltage to vary.

20. An AC power generating system according to any one of claims 16 to 19, including brake control means and means responsive to the output of the two voltage sensors that are

operable to monitor the positive and negative output potentials whereby to connect the brake control means across the intermediate DC voltage to apply a load thereto when the potential difference between the monitored positive and negative potentials rises to a certain level.

- 5 21. An AC power supply generating system according to claim 20, wherein one pair of capacitors are connected in parallel between the positive output potential and the neutral terminal, another pair of capacitors is connected in parallel between the negative output potential and the neutral terminal and the brake control means are connected across the positive and negative output potentials between the capacitors of each pair.

10

22. An AC power supply generating system according to claim 21 when appended to claim 19, wherein the current sensor means that are operable to monitor the DC load current caused by connection of a load across the intermediate DC voltage are connected in either the positive or the negative connection between the brake control means and the capacitors  
15 of each pair that are remote from the two full-wave rectifiers and that are nearer to the inverter means.

23. A method of converting a DC voltage having a positive and negative level of electrical potential into an AC power output in which the positive and negative levels of electrical potential are connected to an AC power output terminal alternately, characterized in that  
20 each connection of one of the positive and negative levels of electrical potential to the output terminal is separated in time from a connection of the other of the positive and negative levels of the electrical potential to the output terminal by an intermediate period of zero voltage.

25

24. A method according to claim 23, wherein the source of each of the positive and negative potentials of DC voltage is a respective charged capacitor and the positive and negative levels of electrical potential are connected alternately to the AC power output terminal through an output filter, both sides of the output filter being connected to neutral  
30 for said intervening periods.

25. A DC to AC converter including a first source of electrical energy operable to be charged with a positive DC potential, a second source of electrical energy operable to be charged with a negative DC potential, first bistable switching means operable in one state to  
35 connect the first electrical energy source to an AC power output terminal through an inductor means of an output filter means and to break that connection in its other state, and

second bistable switching means operable in one state to connect the second electrical energy source to the AC power output terminal through said inductor means and to break that connection in its other state, characterized by third bistable switching means operable in one state to connect said inductor means to neutral and to break that connection in its other state, and control means operable to control operation of said first, said second and said third bistable switching means so that said first and said second bistable switching means are switched to their said one state alternately and are in their said other state when the other of said first and second bistable switching means are in their said one state and said third bistable switching means is switched to its said one state whilst said first and second bistable switching means are both switched to their said other state between each alternate switching of said first and second bistable switching means to their said one state so that the positive and negative levels of electrical potential with which said first and second electrical energy sources are charged when said converter is operated are connected to the AC power output terminal alternately and each connection of the one of the positive and negative levels of electrical potential to the output terminal is separated in time from a connection of the other of the positive and negative levels of electrical potential to the output terminal by an intervening period of zero voltage.

26. A DC to AC converter according to claim 25, wherein the output filter means includes a third chargeable source of electrical energy which is connected on one side to the inductor means and on its other side to neutral, the third bistable switching means being operable to connect the other side of the inductor means to neutral when in its said one state.

27. A DC to AC converter according to claim 25 or claim 26, which is the inverter means of an AC power generating system which includes variable voltage AC power supply generating means, rectifier means having an output and operable to rectify the variable voltage AC power supply to establish an intermediate DC voltage, the inverter means being operable to convert the intermediate DC voltage into the AC power output, there being voltage sensor means operable to monitor the intermediate DC voltage and control means responsive to an output of the voltage sensor means and operable to maintain the intermediate DC voltage at one level.

28. An AC power generating system according to claim 27, wherein the generating means is operable to generate two variable voltage AC power supplies and two full wave rectifiers are provided, each being operable to rectify a respective one of the two variable voltage AC power supplies and each being connected on one side to a neutral terminal and having an



output terminal on its other side, the output terminal of one of the fullwave rectifiers being positive and the output terminal of the other fullwave rectifier being negative such that the two variable voltage AC power supplies are rectified to produce a positive and a negative potential respectively which together comprise the intermediate DC voltage and which are  
5 respectively connected across the first and second electrical energy storage means, there being voltage sensor means severally responsive to a respective one of the positive and negative output potentials of the two fullwave rectifiers and control means operable to maintain each of those positive and negative potentials at a certain level whereby to maintain the intermediate DC voltage at one level.

10

29. A DC to AC converter according to claim 27 or claim 28, wherein there are two booster circuits, each connected between a respective one of the output terminals of the two fullwave rectifiers and the neutral terminal and each operable to boost the respective one of the output electrical potentials, each of the control means being operatively associated with a respective  
15 one of the booster circuits.

30. A method of operating an AC power generating system of the kind which includes an engine driven generator which provides a variable voltage electrical output, rectifier means operable to rectify the variable voltage electrical output, booster means  
20 operable to boost the voltage of the variable voltage electrical output and thereby to provide an intermediate DC voltage, inverter means which are operable to convert the intermediate DC voltage into an AC power output, characterized by means operable to maintain the intermediate DC voltage at one level up to a predetermined speed of the engine driven generator which is less than the maximum speed thereof, wherein the means operable to  
25 maintain the intermediate DC voltage at said one level is disabled when the speed of the engine driven generator rises to at least said predetermined speed and the speed of the engine driven generator and the intermediate DC voltage are allowed to rise so that, when the engine driven generator is running at maximum speed, the AC power output is allowed to increase until a balance is achieved.

30

31. A method of operating an AC power generating system including the steps of:-  
(i) operating a variable speed prime mover driven generator to generate a variable voltage AC power supply;  
(ii) rectifying the variable voltage AC power supply to establish an intermediate DC  
35 voltage;  
(iii) monitoring the intermediate DC voltage and responding to transient variations therein

whereby to maintain it at a substantially constant level; and

(iv) operating an inverter to convert the intermediate DC voltage into an AC power output; characterised by the further step of

- (v) monitoring DC current flow to the inverter; wherein the monitored DC voltage and DC current are compared with a number of references whereby, whenever one of the monitored DC voltage and DC current deviates from an acceptable normal level at least transiently, an appropriate control correction is applied automatically, the nature of the applied control correction depending on the severity of the deviation from the acceptable level.

32. A method of operating an AC power generating system including the steps of:-

- (i) operating a variable speed prime mover driven generator to generate a variable voltage AC power supply;
- (ii) rectifying the variable voltage AC power supply to establish an intermediate DC voltage;
- (iii) controlling the intermediate DC voltage to maintain it at a substantially constant level;
- (iv) operating an inverter to convert the intermediate DC voltage into an AC power output;
- (v) monitoring the intermediate DC voltage; and
- (vi) controlling operation of the prime mover driven generator by feedback control so as to increase its speed when the monitored intermediate DC voltage falls to the level of the first reference voltage whereby to restore the intermediate DC voltage to said substantially constant level;
- characterised by the further steps of
- (vii) comparing the intermediate DC voltage with a first reference voltage which is lower than the substantially constant level; and
- wherein
- (viii) the intermediate DC voltage is also compared with a second reference voltage which is lower than said first reference voltage, and
- (ix) operation of the inverter is controlled so as to reduce the voltage and/or frequency of the AC power output when the monitored intermediate DC voltage falls to the level of the second reference voltage whereby to provide a transient off-loading effect which assists restoration of the intermediate DC voltage to said substantially constant level.

33. A method of operating an AC power generating system according to claim 32, including the further steps of:-

- a) monitoring a DC load current which results from connection of a load across the intermediate DC voltage;

- b) comparing the monitored DC load current with a certain current reference level; and
  - c) further controlling operation of the prime mover driven generator by feedback control when the monitored DC load current exceeds the certain current reference level whereby to further increase the speed of the prime mover driven generator by an amount which is
- 5 proportional to the amount by which the monitored DC load current exceeds the certain current level.

34. A method of operating an AC power generating system according to claim 32 or claim 33, including the further step of controlling operation of the inverter so as to increase the

10 frequency of the AC power supply when the monitored DC voltage rises to the level of a third reference voltage.

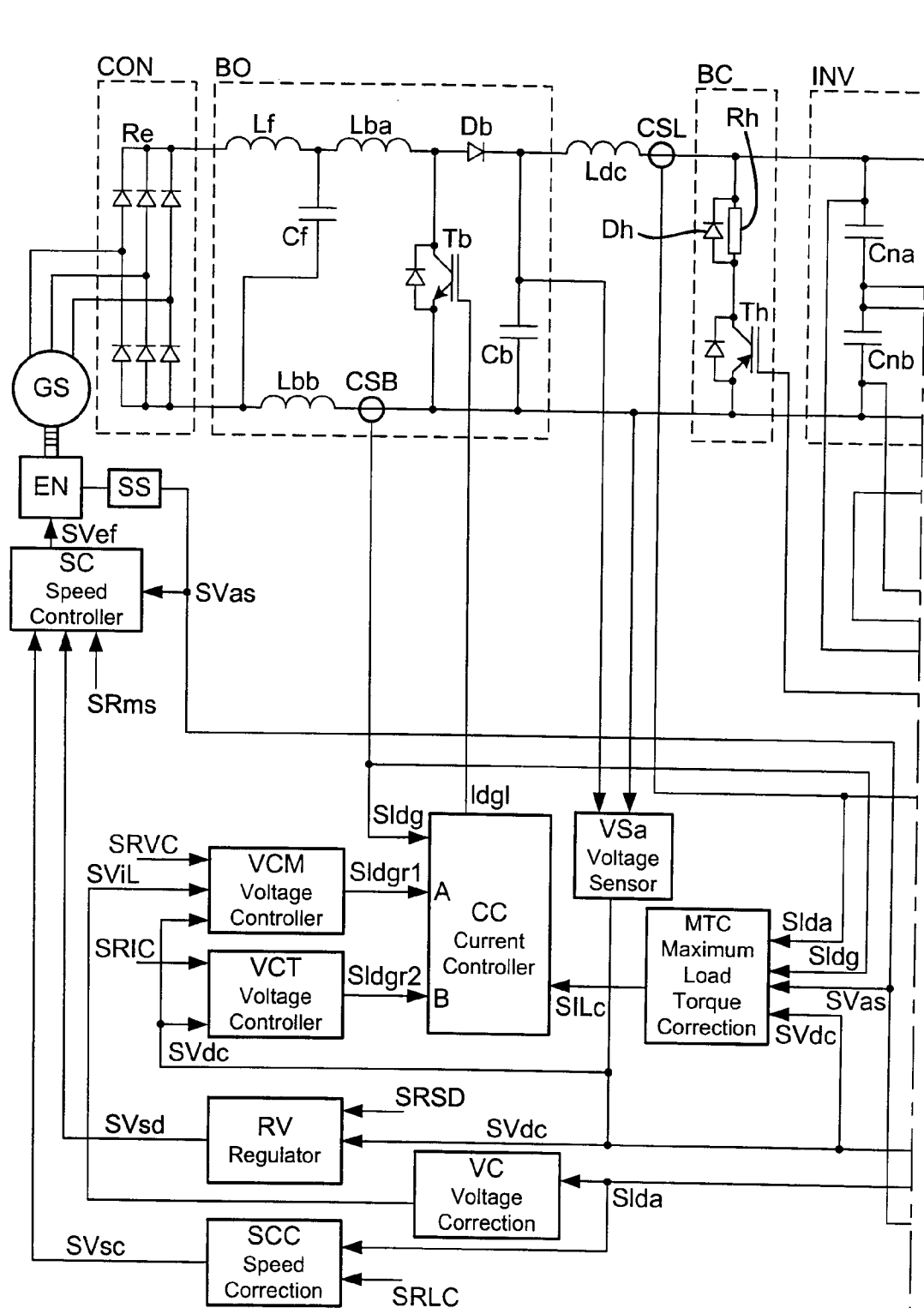
35. A method of operating an AC power generating system according to any one of claims 32 to 34 including the further step of connecting a brake controller across the intermediate

15 DC voltage to apply a load thereto when the monitored DC voltage rises to a certain voltage level.

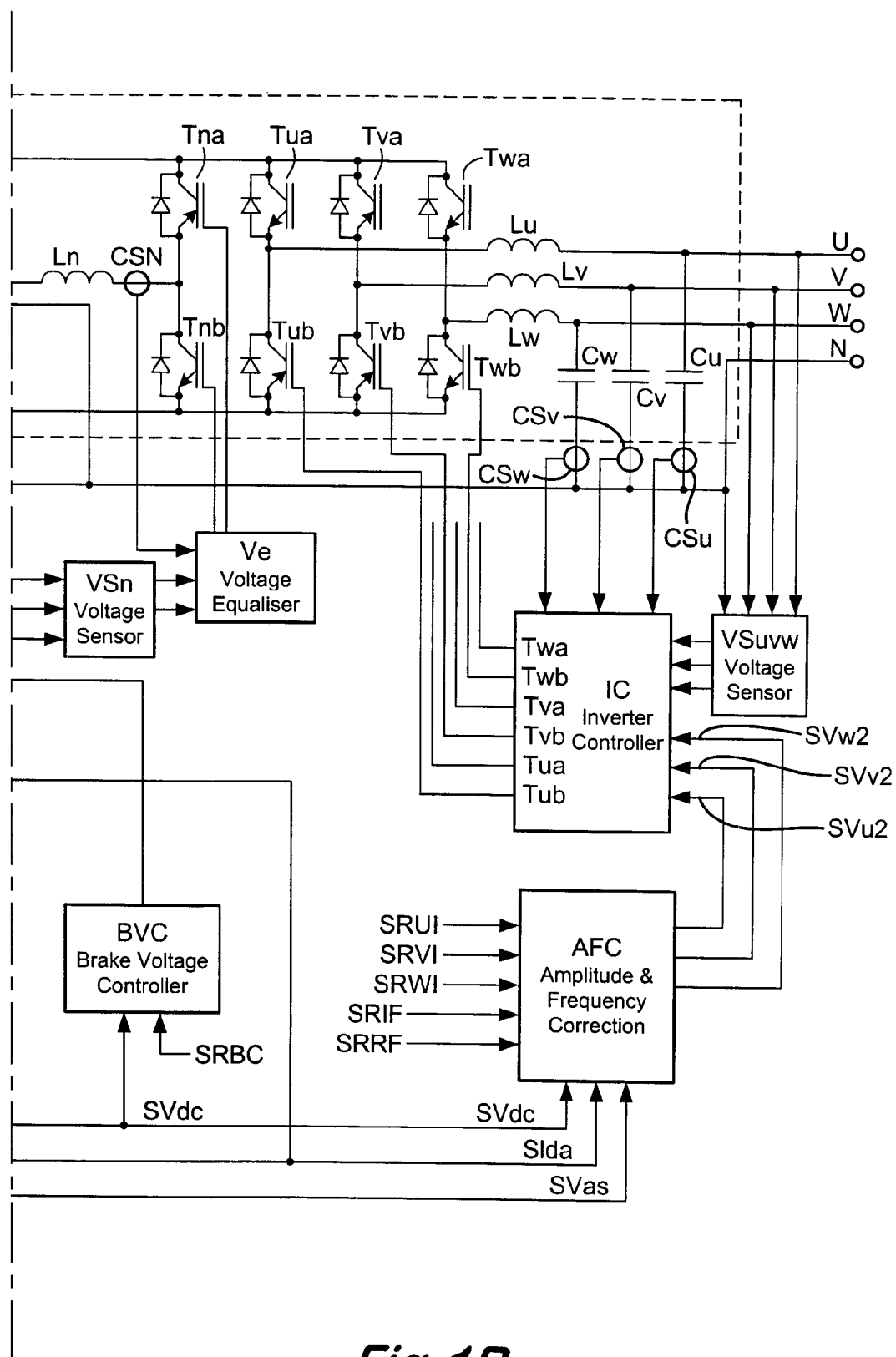
36. A method of operating an AC power generating system according to claim 35 when appended to claim 34, wherein the third reference voltage is higher than said certain voltage

20 level.

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*Fig. 1A*

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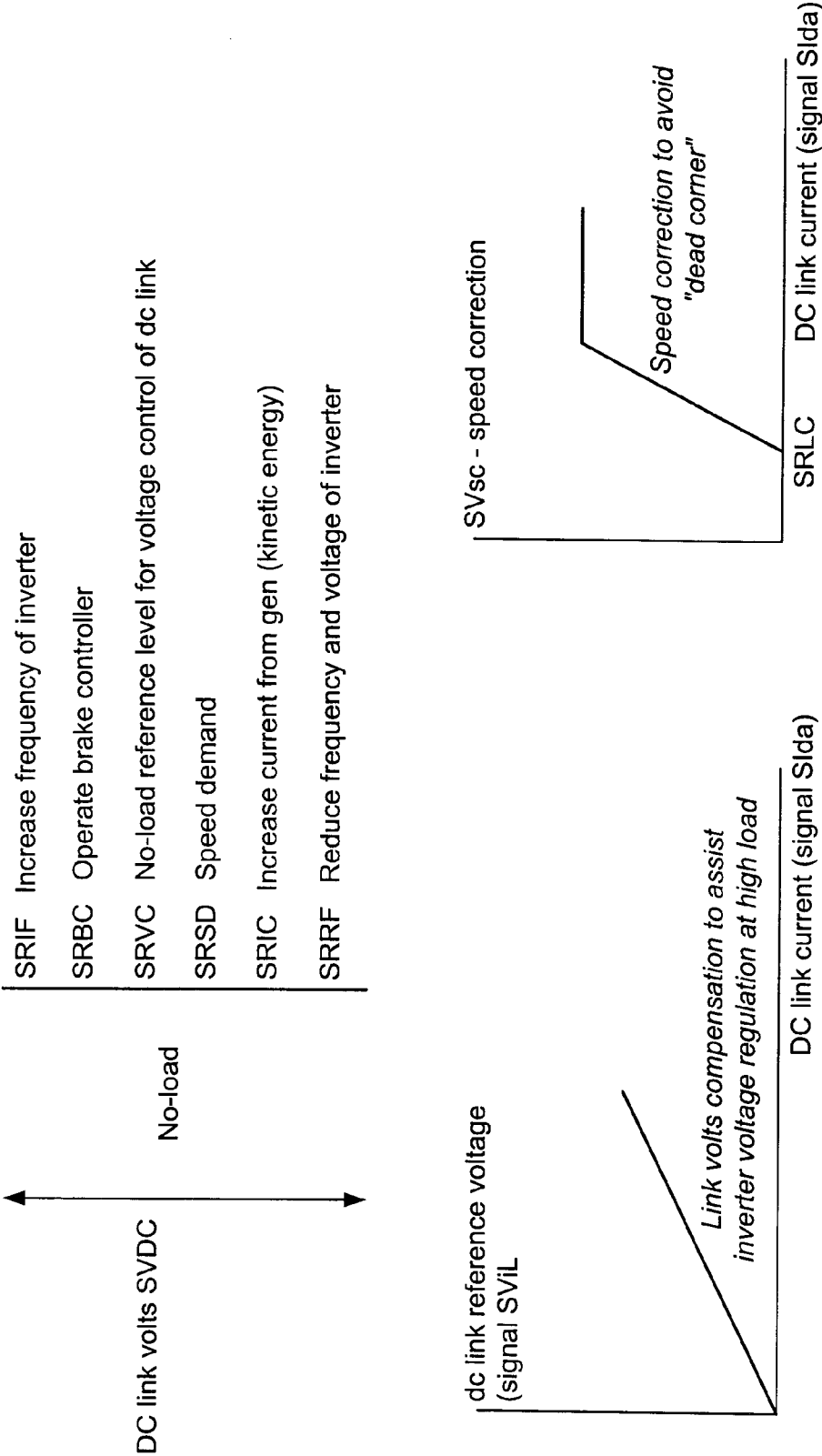


Fig.2

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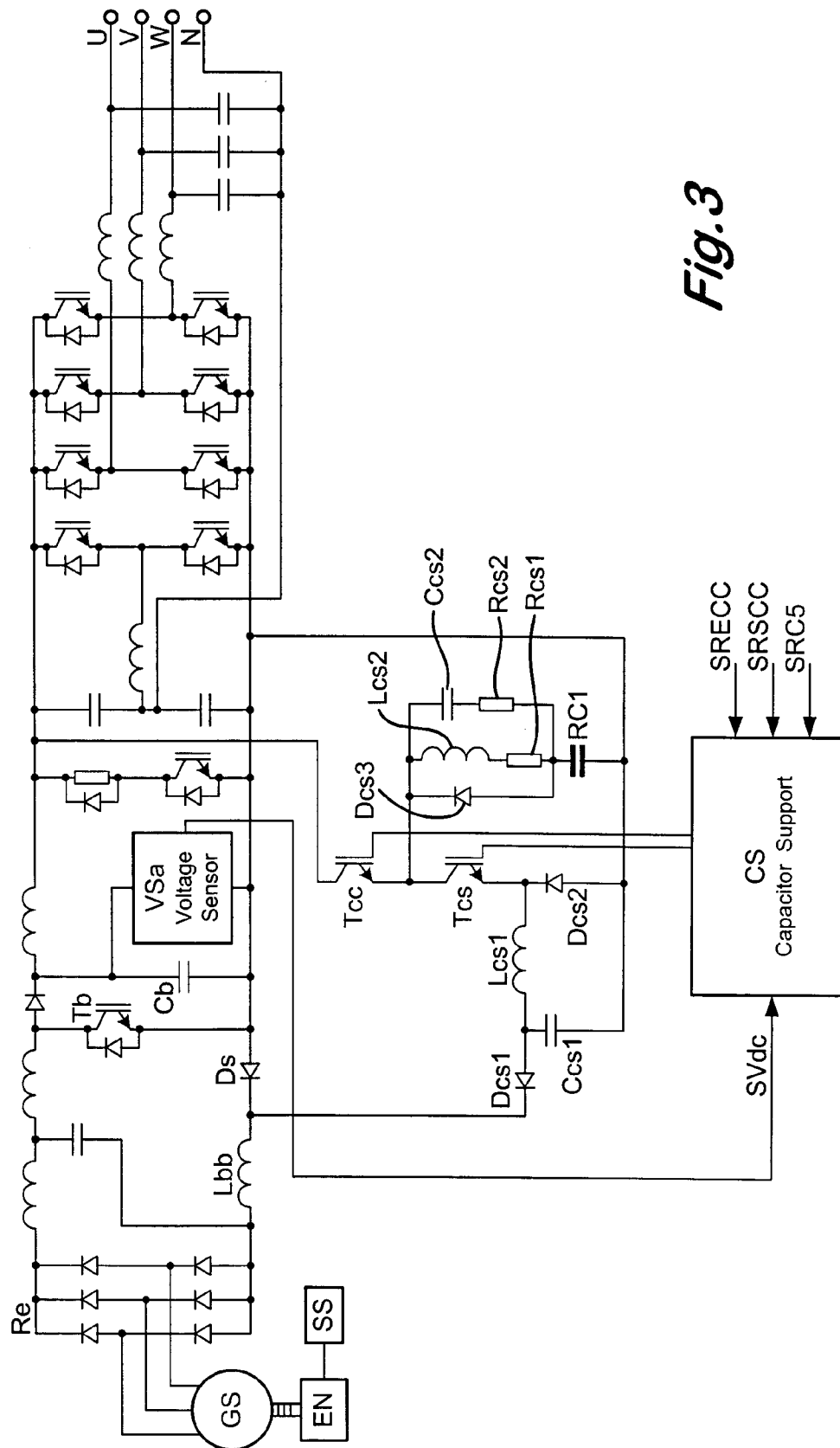


Fig. 3

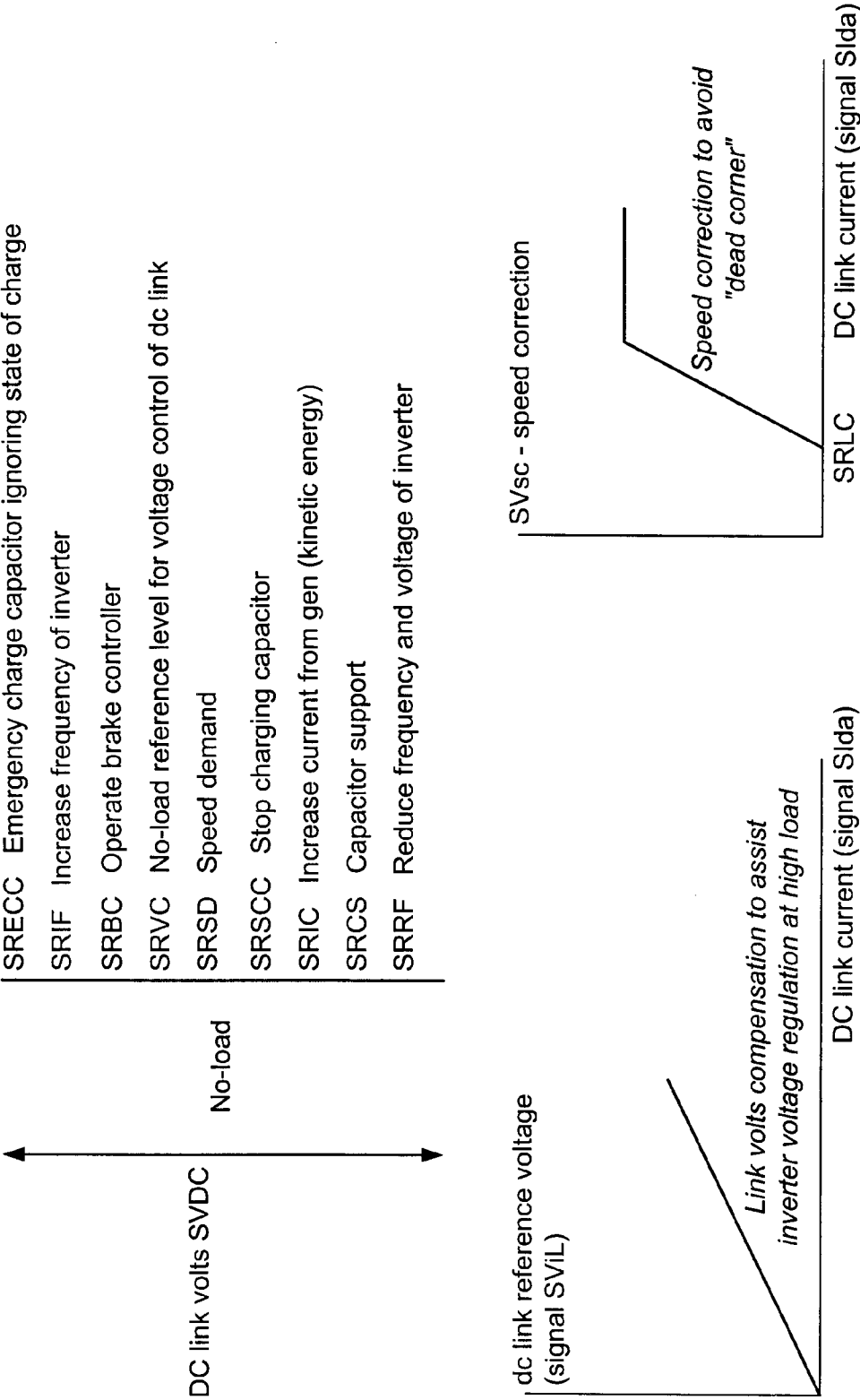
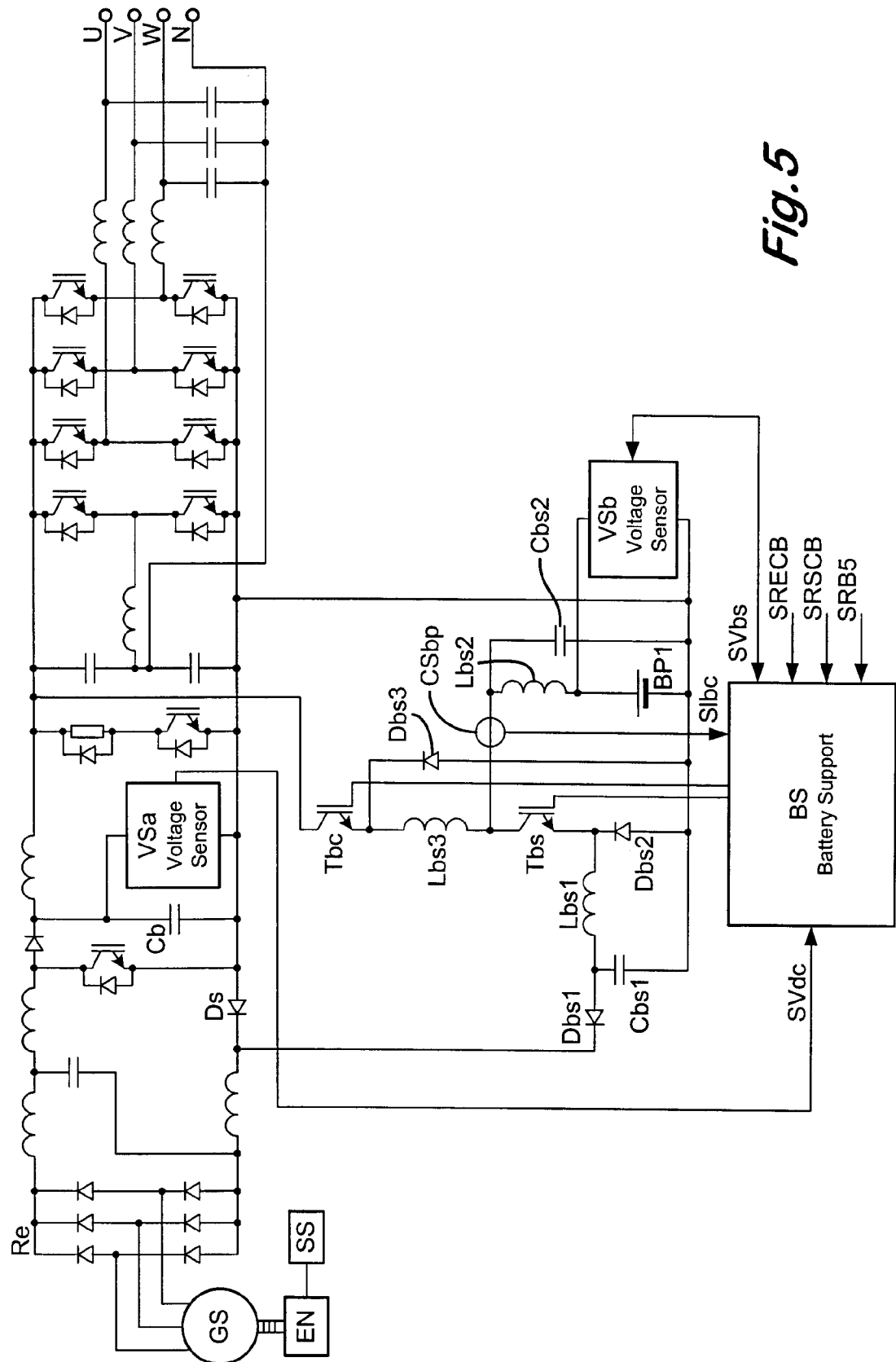


Fig.4



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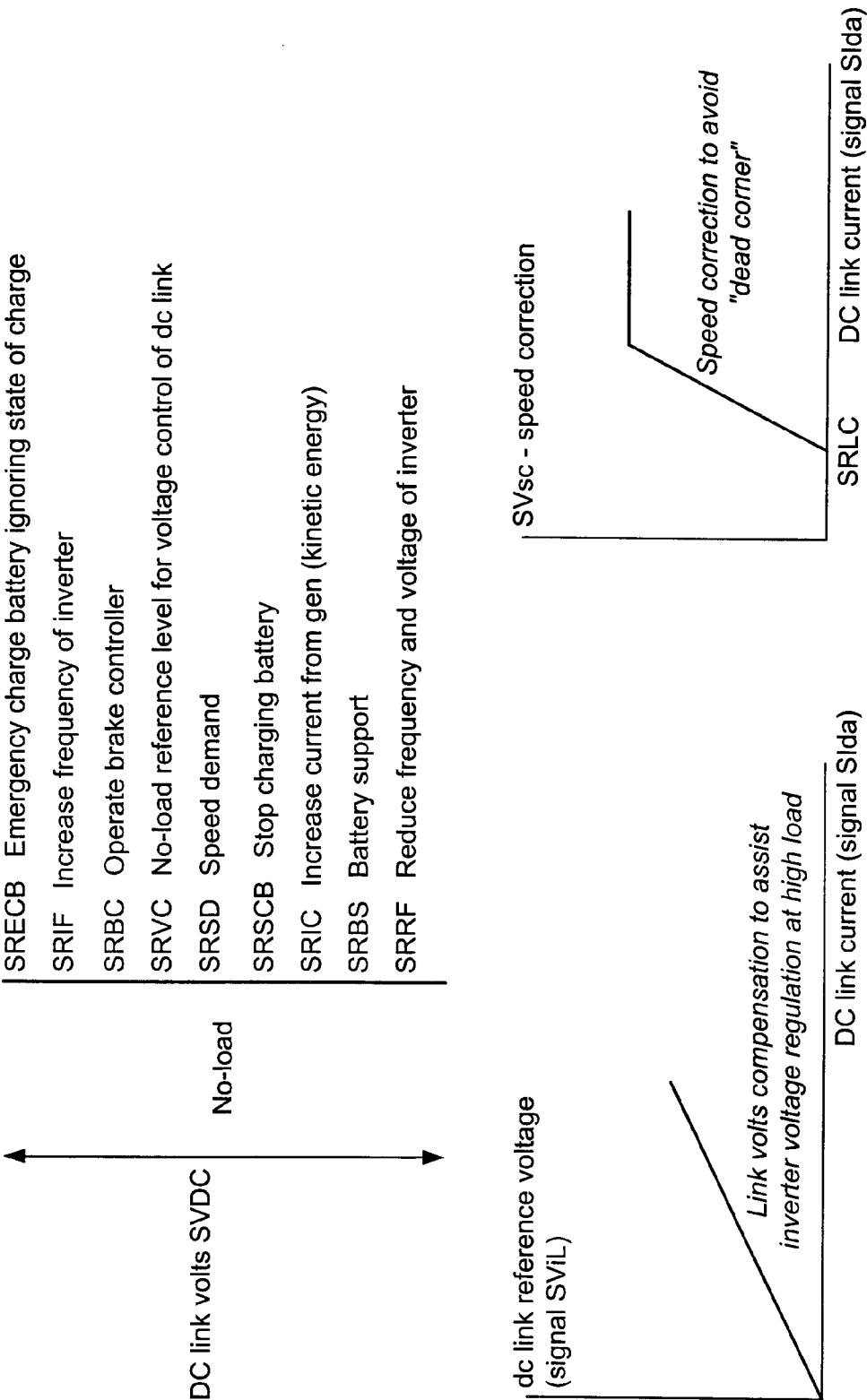
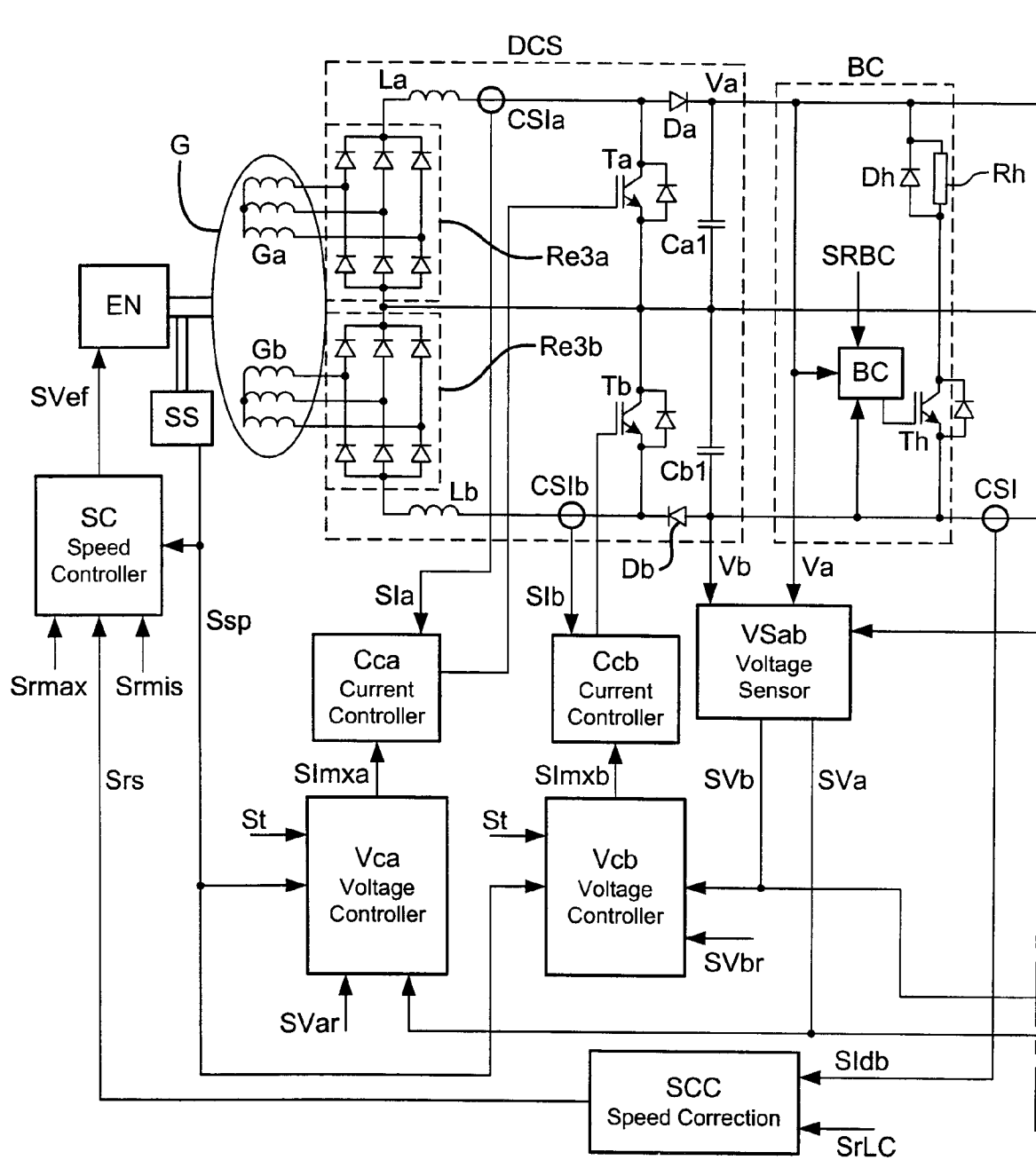
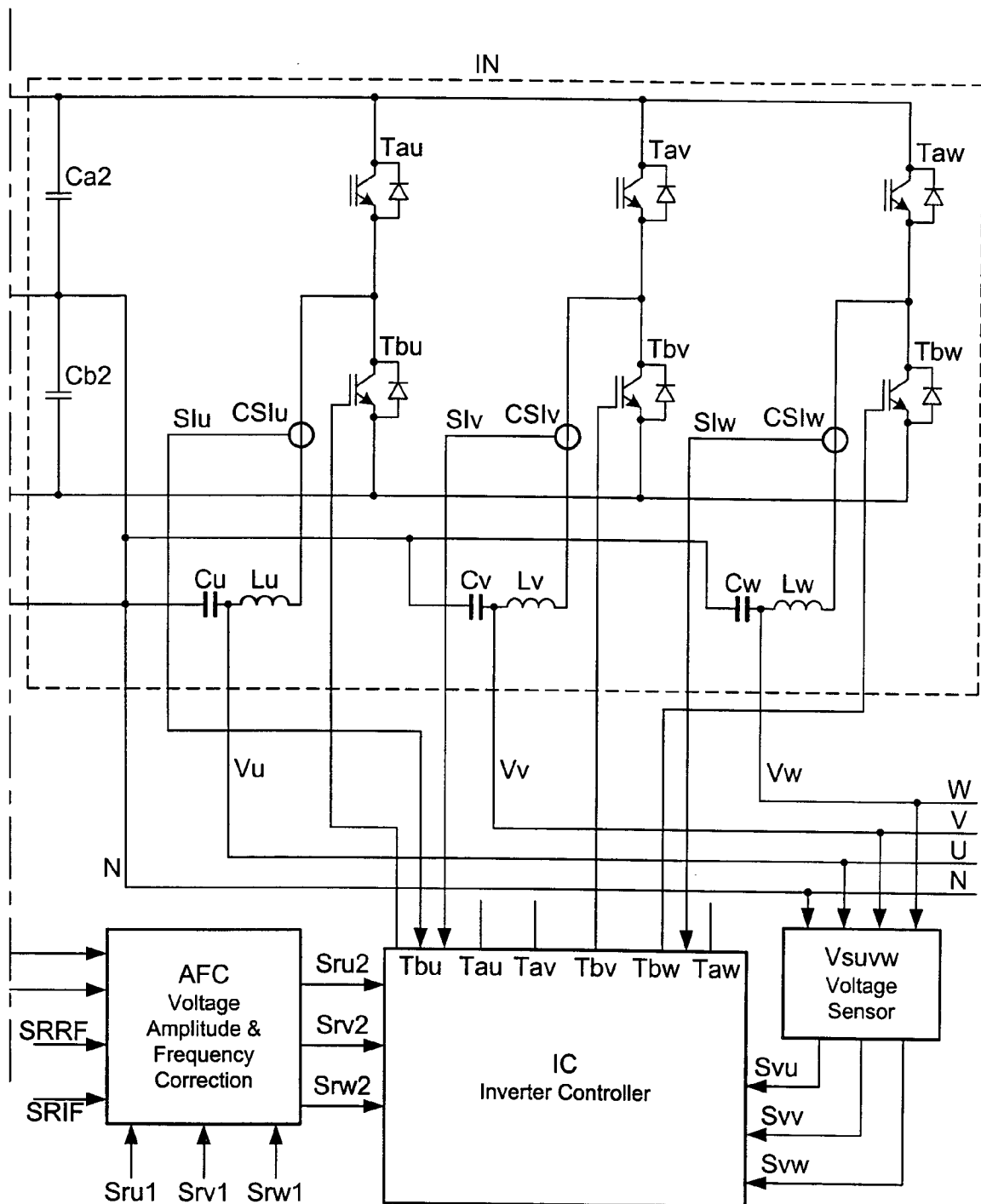


Fig.6

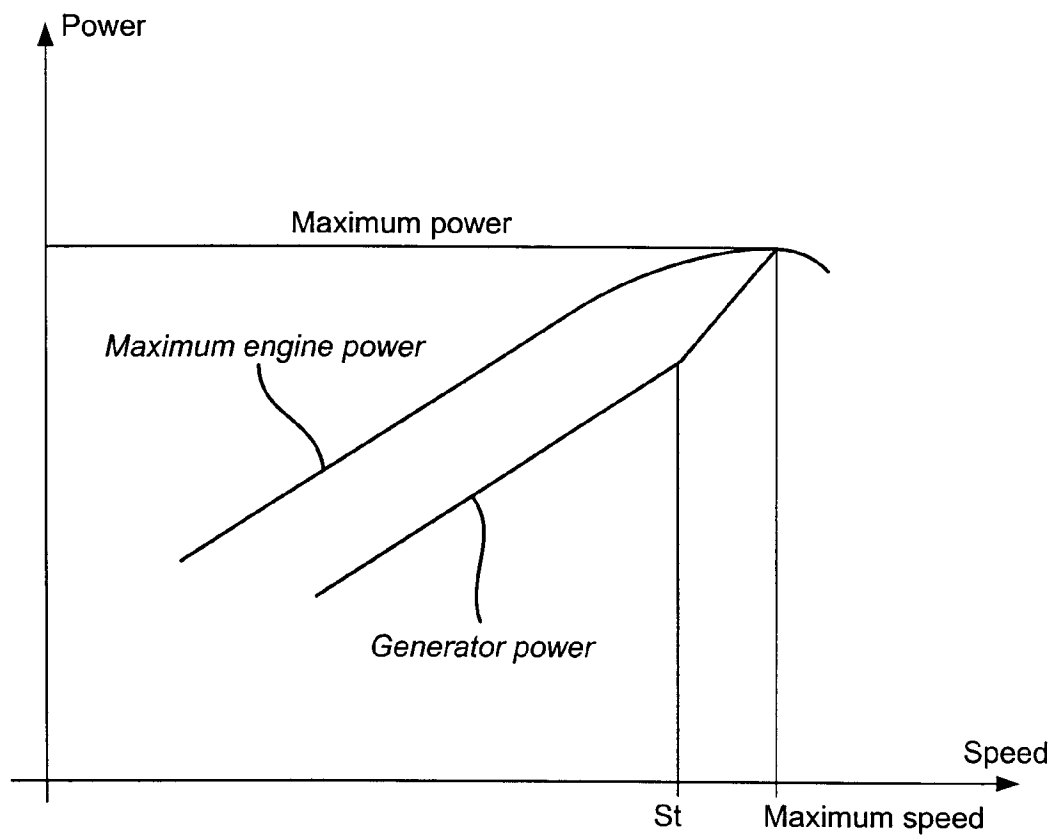
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*Fig. 7A*

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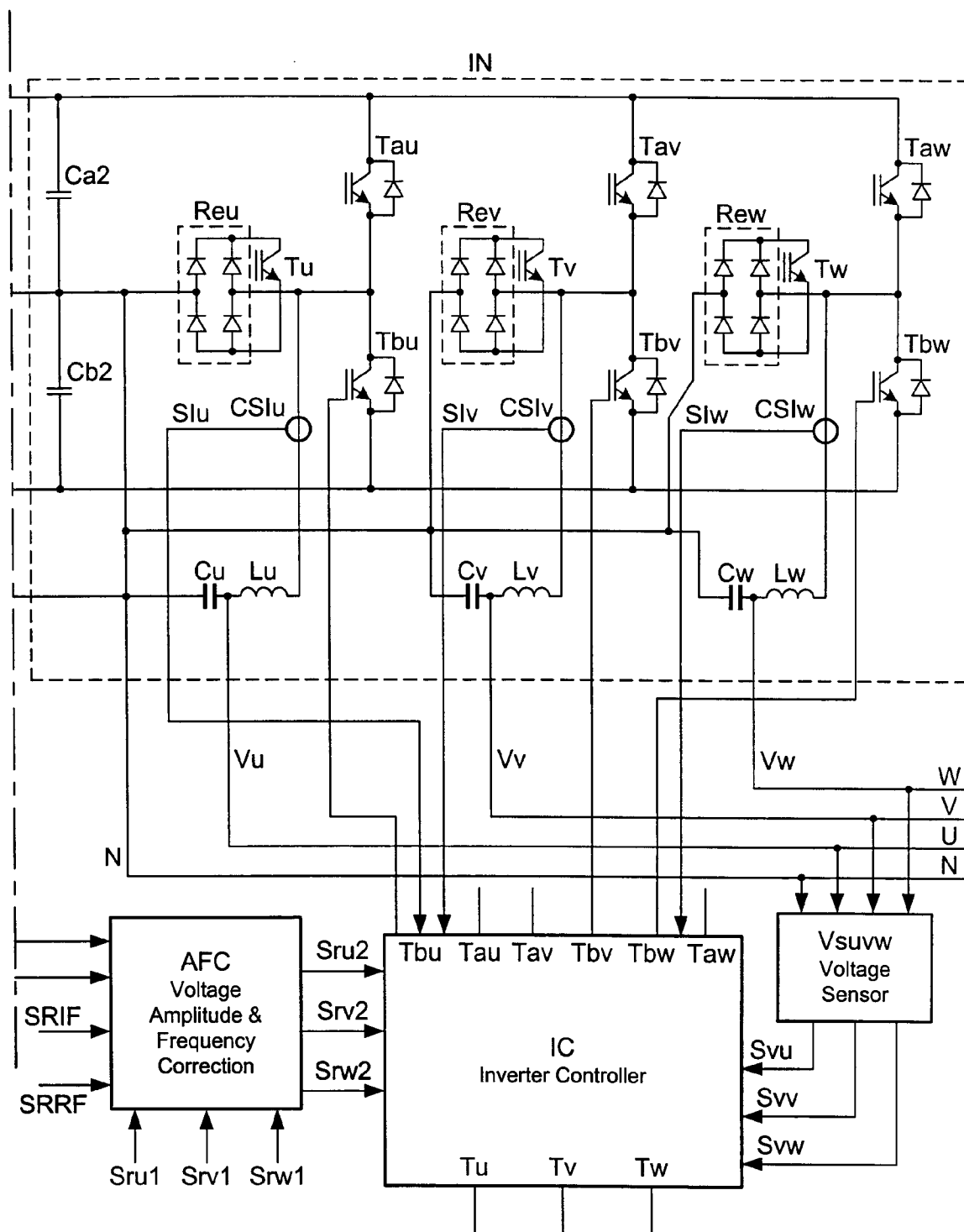
*Fig. 7B*

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***Fig.8***



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**Fig.9B**

# INTERNATIONAL SEARCH REPORT

International Application No

PCT/GB 01/00169

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H02J9/06 H02P9/48 H02M5/451

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H02J H02P H02M

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

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Y	page 3, line 16 - line 27	16-30
A	page 7, line 19 -page 11, line 26; figure 1	1-12,14, 15,31-36
	page 19, line 2 - line 20	
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A	column 7, line 14 - line 22; figure 4	1-12, 14-19, 32-36
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Date of the actual completion of the international search

8 June 2001

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18/06/2001

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International Application No

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